

 ICOM

SERVICE MANUAL

UHF FM TRANSCEIVER

IC-F2610

INTRODUCTION

This service manual describes the latest service information for the **IC-F2610** UHF FM TRANSCEIVER.

To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

DANGER

NEVER connect the transceiver to an AC outlet or to a DC power supply that uses more than 16 V. This will ruin the transceiver.

DO NOT expose the transceiver to rain, snow or any liquids.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100 mW) to the antenna connector. This could damage the transceiver's front end.



ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit order numbers
2. Component part number and name
3. Equipment model name and unit name
4. Quantity required

<SAMPLE ORDER>

1110003571 S.IC MC3372SVM IC-F2610 MAIN UNIT 5 pieces
8810008660 Screw BT M3x8 NI-ZU IC-F2610 Bottom cover 10 pieces
Addresses are provided on the inside back cover for your convenience.

REPAIR NOTES

1. Make sure a problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated tuning tool MUST be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 40 dB to 50 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.

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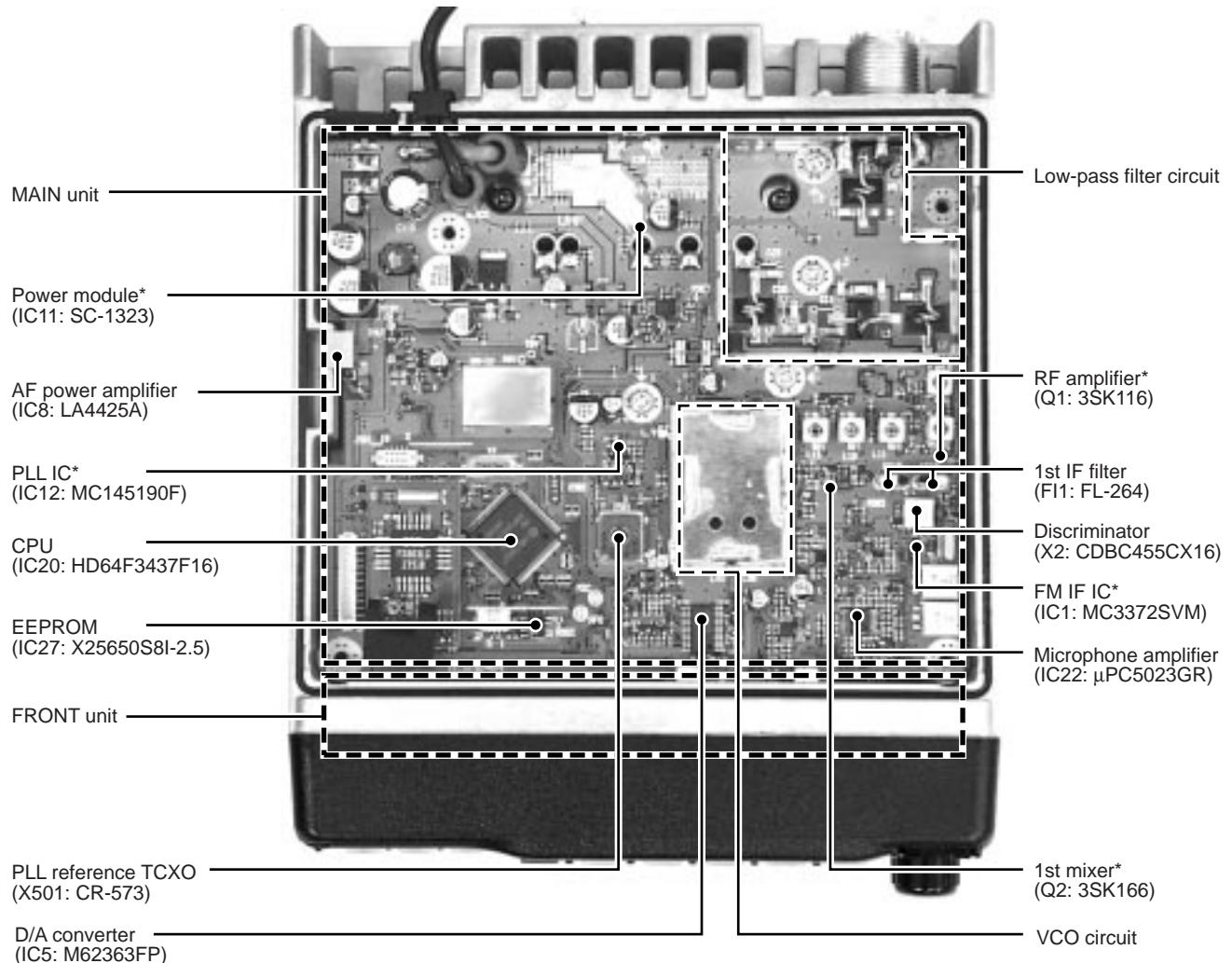
SECTION 1 SPECIFICATIONS

GENERAL	Mesurement method	ETS 300 086								
	Frequency coverage	440–490 MHz								
	Number of channels	128 (16 ch × 8 banks)								
	Type of emission	16K0F3E (25 kHz; Wide) 8K50F3E (12.5 kHz; Narrow)								
	Operating temperature range	−20°C to +55°C								
	Power supply voltage	13.2 V DC (negative ground)								
	Current drain (approx.)	<table border="1"> <tr> <td>TX</td> <td>max. power</td> <td>8.0 A</td> </tr> <tr> <td>RX</td> <td>max. audio</td> <td>1.2 A</td> </tr> <tr> <td></td> <td>stand-by</td> <td>700 mA</td> </tr> </table>	TX	max. power	8.0 A	RX	max. audio	1.2 A		stand-by
TX	max. power	8.0 A								
RX	max. audio	1.2 A								
	stand-by	700 mA								
Antenna connector	SO-239 (50 Ω)									
Dimensions (proj. not included)	150(W) × 50(H) × 180(D) mm									
Weight	1.5 kg									
Output power	25 W									
TRANSMITTER	Modulation system	Variable reactance frequency modulation								
	Max. frequency deviation	±5.0 kHz (Wide) ±2.5 kHz (Narrow)								
	Frequency error	±1.5 kHz								
	Spurious emissions	0.25 μW								
	Adjacent channel power	70 dB (Wide) 60 dB (Narrow)								
	Audio frequency response	+2 dB to −5 dB of 6 dB/octave range from 300 Hz to 3000 Hz (Wide)/2550 Hz (Narrow)								
	Audio harmonic distortion	5 % (40 % Dev.)								
	Residual modulation (with CCITT filter)	45 dB minimum: 55 dB typical (Wide) 40 dB minimum: 50 dB typical (Narrow)								
	Limitting	70–100 % of modulation								
	Microphone connector	8-pin modular (600 Ω)								
RECEIVER	Intermediate frequency	1st: 30.875 MHz 2nd: 455 kHz								
	Sensitivity	−2 dB _P V emf (at 20 dB SINAD)								
	Squelch sensitivity	−4 dB _P V emf (Threshold)								
	Adjcent channel selectivity	70 dB (Wide) 60 dB (Narrow)								
	Spurious response	70 dB								
	Intermodulation	70 dB typical								
	Audio frequency response	+2 dB to −5 dB of 6 dB/octave range from 300 Hz to 3000 Hz (Wide)/2550 Hz (Narrow)								
	Hum and noise (with CCITT filter)	45 dB minimum: 55 dB typical (Wide) 40 dB minimum: 50 dB typical (Narrow)								
	Audio output power	3.5 W at 10% distortion with a 4 Ω load								
	External SP connector	2-conductor 3.5 (d) mm (1/8")/4 Ω								

All stated specifications are subject to change without notice or obligation.

SECTION 2

INSIDE VIEW

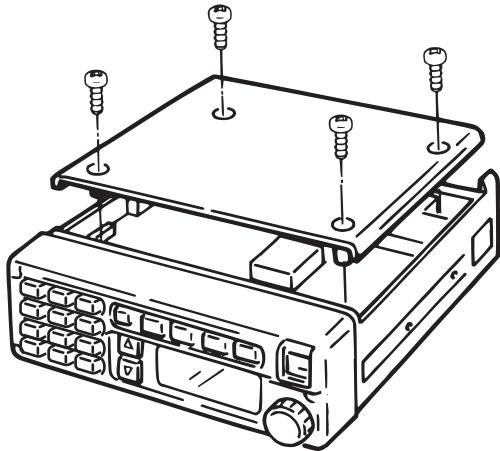


*Located under side of the point

SECTION 3 DISASSEMBLY AND OPTION INSTRUCTIONS

- **Opening cover**

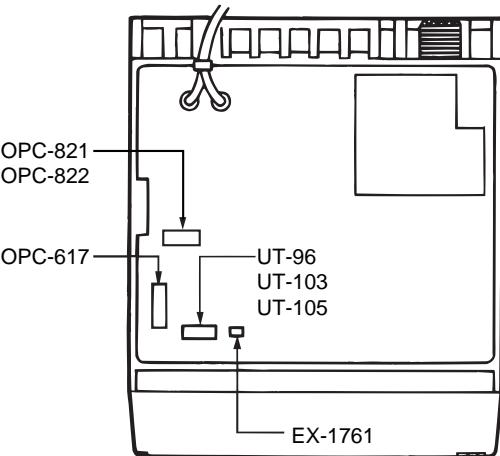
Remove 4 screws from bottom cover.



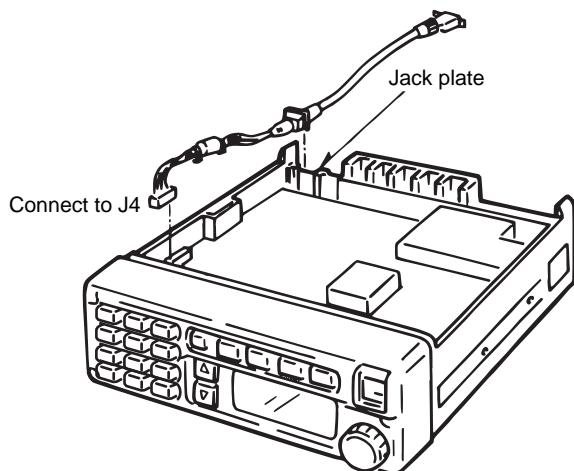
- **Installation location**

Install option units.

OPC-617 ACC CABLE: J4
UT-96 5-TONE UNIT UT-103 FFSK Logic Board or UT-105 SmarTrunk II™ Logic Board J6
EX-1761 MEMORY EXPANSION UNIT: J7
OPC-821 AVL CONNECT CABLE or OPC-822 INTERFACE CABLE J501 J501

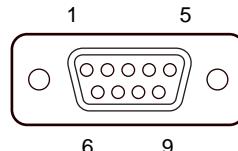


- **OPC-617 connection**



Break the jack plate using cutting pliers to connect the OPC-617.

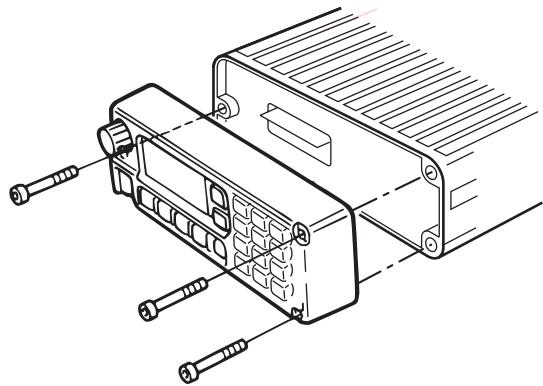
PIN ASSIGNMENT



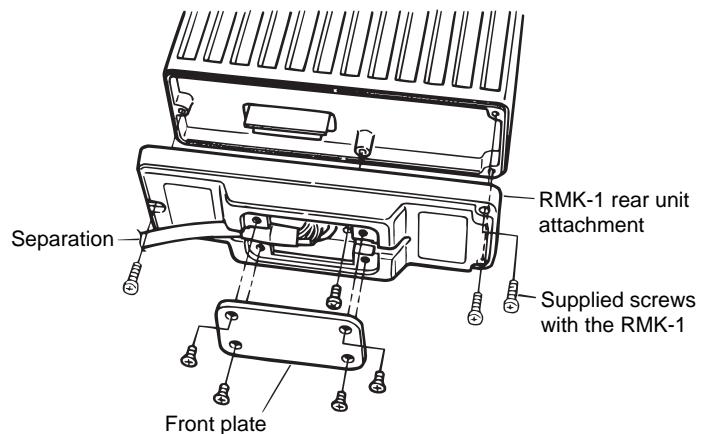
Pin No.	Terminal name	Description	Specification
1	DIM	Backlight control input	+5 to +30 V for dark
2	PAAF	AF output for public address and Ext SP functions	0 to 330 mV rms/47 kΩ
3	DISC	AF output for a terminal unit	330 mV rms/100 kΩ
4	IN	AF input for a terminal unit	330 mV rms/1200 bps
5	PTT	PTT control input	0 V for transmit
6	HORN	Grounded when receiving the specified call	Less than 50 mA when grounded
7	PAAF ⊖	Ground for PAAF	—
8	DISC ⊖	Ground for terminal output	—
9	IN ⊖	Ground for terminal input	—

• **RMK-1 connection**

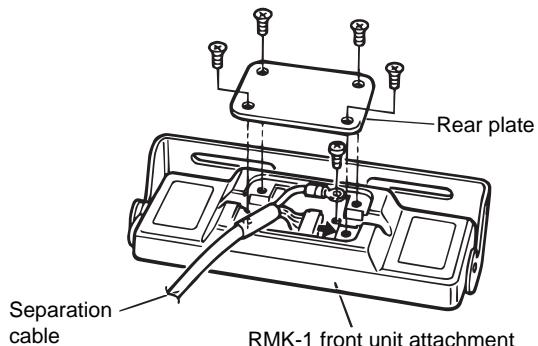
- ① Remove 3 allen-socket bolts from the front plate using an allen-wrench (1/32 in).
- ② Separate the front unit from the transceiver main unit.



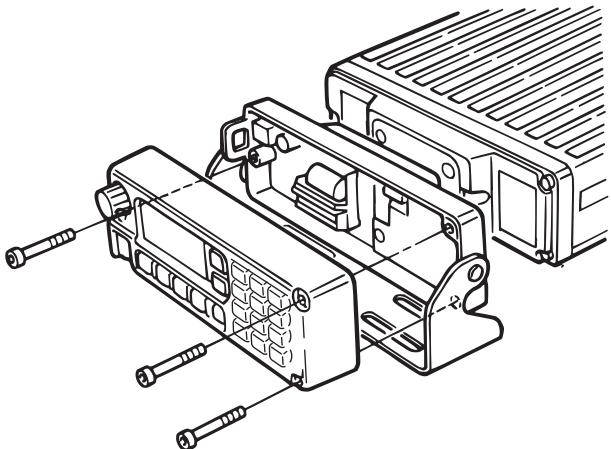
- ③ Attach the 'RMK-1 rear unit attachment' to the transceiver main unit using the supplied screws.
- ④ Remove 4 screws from the attachment to open the front plate.
- ⑤ Connect an optional separation cable OPC-609 (1.9 m) to the inside of the front plate and tighten the cable lug using the screw.
- ⑥ Re-attach the front plate.



- ⑦ Remove 4 screws from the 'RMK-1 front unit attachment' to open the rear plate.
- ⑧ Connect the other end of the optional separation cable to the attachment and tighten the cable lug using the screw.
- ⑨ Re-attach the rear plate.



- ⑩ Attach the front unit and attachment with the 3 removed allen-socket bolts.



SECTION 4 CIRCUIT DESCRIPTION

4-1 RECEIVER CIRCUITS

4-1-1 ANTENNA SWITCHING CIRCUIT

The antenna switching circuit functions as a low-pass filter while receiving and a resonator circuit while transmitting. The circuit does not allow transmit signals to enter receiver circuits.

Received signals enter the antenna connector (J1) and pass through the low-pass filters (L1–L3, C2, C3, C8–C10, C415). The filtered signals are passed through the $\lambda/4$ type antenna switching circuit (D5, D6, L5) and then applied to the RF circuit.

4-1-2 RF CIRCUIT

The RF circuit amplifies signals within the range of frequency coverage and filters out-of-band signals.

The signals from the antenna switching circuit pass through the two-stage tunable bandpass filters (D7, D8, L8, L9). The filtered signals are amplified at the RF amplifier (Q1) and then enter the another three-stage bandpass filters (D9–D11, D514, L12, L13, L507) to suppress unwanted signals. The filtered signals are applied to the 1st mixer circuit.

D7–D11 and D514 employ varactor diodes, that are controlled by the PLL lock voltage, to track the bandpass filter. These varactor diodes tune the center frequency of an RF pass band for wide bandwidth receiving and good image response rejection.

4-1-3 1ST MIXER AND 1ST IF CIRCUITS

The 1st mixer circuit converts the received signal to fixed frequency of the 1st IF signal with the PLL output frequency. By changing the PLL frequency, only desired frequency will pass through a pair of crystal filters at the next stage of the 1st mixer.

The RF signals from the bandpass filter are mixed with the 1st LO signals, which come from the Rx VCO circuit via the buffer amplifier (Q3), at the 1st mixer circuit (Q2) to produce a 30.875 MHz 1st IF signal. The 1st IF signal is passed

through the matching circuit (L14, L15) and a pair of crystal filters (FI1a/b) in order to obtain selection capability and to pass only the desired signals. The filtered signal is applied to the 2nd IF circuit after being amplified at the 1st IF amplifier (Q4).

4-1-4 2ND IF AND DEMODULATOR CIRCUITS

The 2nd mixer circuit converts the 1st IF signal into a 2nd IF signal. The double-conversion superheterodyne system (which convert receive signals twice) improves the image rejection ratio and obtains stable receiver gain.

The 1st IF signal from the IF amplifier (Q4) is applied to the 2nd mixer section of the FM IF IC (IC1, pin 16) and is then mixed with the 2nd LO signal for conversion into 455 kHz 2nd IF signal.

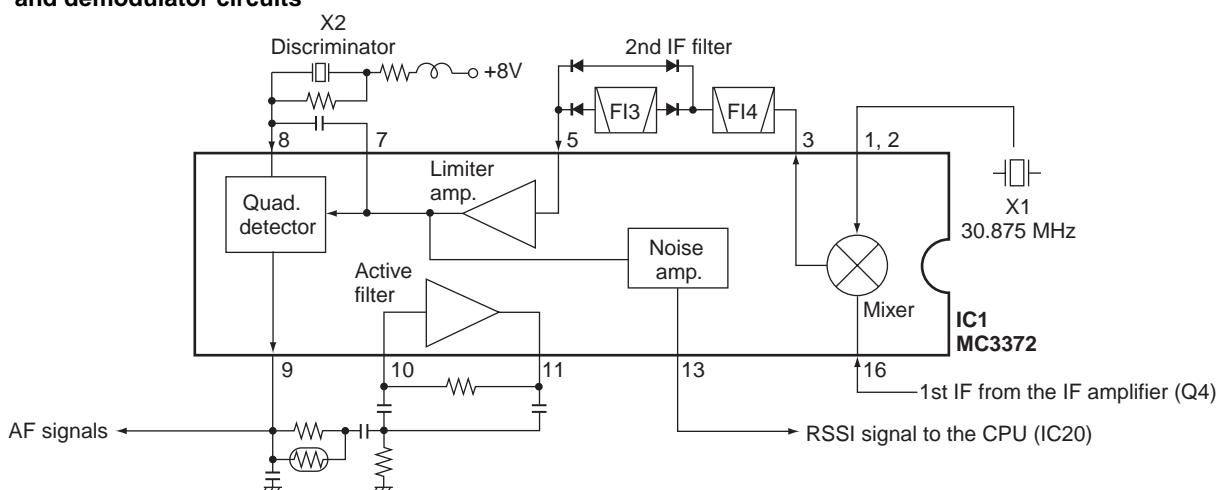
IC1 contains the 2nd mixer, 2nd local oscillator, limiter amplifier, quadrature detector, active filter and noise amplifier circuit. The local oscillator section and X1 generates the 30.420 MHz 2nd LO signal.

The 2nd IF signal from the 2nd mixer (IC1, pin 3) passes through the ceramic filters (FI3 and FI4) during narrow channel spacing selection or passes through FI4 (bypassing FI3) only during wide channel spacing selection to suppress unwanted heterodyne frequencies signals via the N/W switches (D501, D502).

The filtered signal is applied to the quadrature detector section in the FM IF IC to demodulate the 2nd IF signal into AF signals using the ceramic discriminator (X2) after being amplified at the limiter amplifier section (pin 5). The demodulated AF signals are output from pin 9 of the IC and applied to the AF circuit via the receiver mute circuit.

The N/W switches (D501, D502) select a ceramic filter (FI3 or bypass), and the other N/W switch (Q61) adjusts the input level of the FM IF IC (IC1, pin 8) to switch the bandwidth depending on the NWC signal from the CPU (IC20, pin 57). When NWC signal is high level, bandwidth setting is wide.

• 2nd IF and demodulator circuits



4-1-5 AF AMPLIFIER CIRCUIT

The AF amplifier circuit amplifies the demodulated AF signals to drive a speaker.

The AF signals from the FM IF IC (IC1, pin 9) are amplified at the AF amplifier (IC2), and are then applied to the high-path filter circuit (IC3a/b). The high-pass filter characteristics are controlled by the HFSW signal from the CPU (IC20, pin 60). When HFSW signal is high level, the cut-off frequency is shifted higher to remove CTCSS or DTCS signals.

The filtered AF signals from the high-pass filter (IC3, pin 1) are passed through the de-emphasis circuit (R68, C74) with frequency characteristics of -6 dB/octave , and are then applied to the electronic volume controller (IC7, pin 2) via the AF mute switch (Q6).

The output AF signals from the electronic volume controller (IC7, pin 9) are applied to the AF power amplifier (IC8) to drive the speaker.

4-1-6 RECEIVE MUTE CIRCUITS

• NOISE SQUELCH

A squelch circuit cuts out AF signals when no RF signals are received. By detecting noise components in the AF signals, the squelch circuit switches the AF mute switch.

Some noise components in the AF signals from the FM IF IC (IC1, pin 9) are passed through the active filter section in the IC (pins 10, 11). The N/W switch (Q62) adjusts the input noise level to the IC between wide and narrow bandwidths. When NWC signal which is applied to Q62 is high level, bandwidth setting is wide.

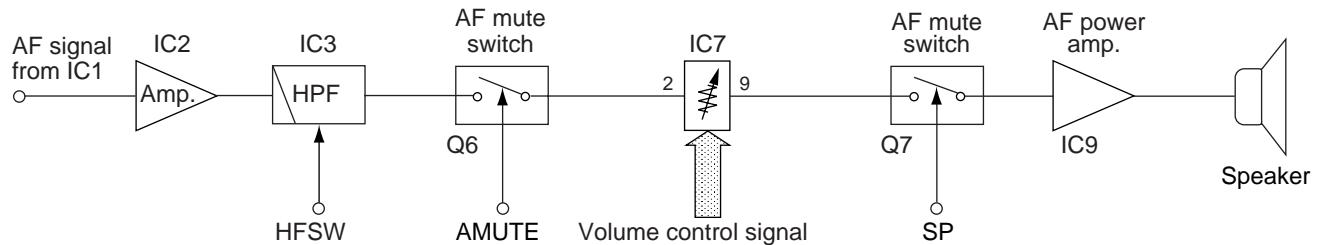
The noise signals from FM IF IC (IC1, pin 11) are passed through the level controller (IC5, pins 21, 22) and are then converted into the pulse-type signals (NOIS) at the noise detector circuit (Q9, Q10).

The NOIS signal from the noise detector (Q10) is applied to the CPU (IC20, pin 32). The CPU then analyzes the noise condition and controls the AMUT (pin 56) and SP (pin 79) ports to toggle the AF mute switches (Q6, Q7).

• CTCSS AND DTCS

The tone squelch circuit detects AF signals and opens the squelch only when receiving a signal containing a matching subaudible tone (CTCSS or DTCS). When tone squelch is in use, and a signal with a mismatched or no subaudible tone is received, the tone squelch circuit mutes the AF signals even when noise squelch is open.

• AF circuit



A portion of the AF signals from the AF amplifier (IC2) pass through the low-pass filter (Q503) and are then applied to the CTCSS decoder inside the CPU (IC20, pin 43) via the CDEC line to control the AMUT and SP ports.

4-2 TRANSMITTER CIRCUITS

4-2-1 MICROPHONE AMPLIFIER CIRCUIT

The microphone amplifier circuit amplifies audio signals from the microphone, within $+6 \text{ dB/octave}$ pre-emphasis characteristics, to a level needed for the modulation circuit.

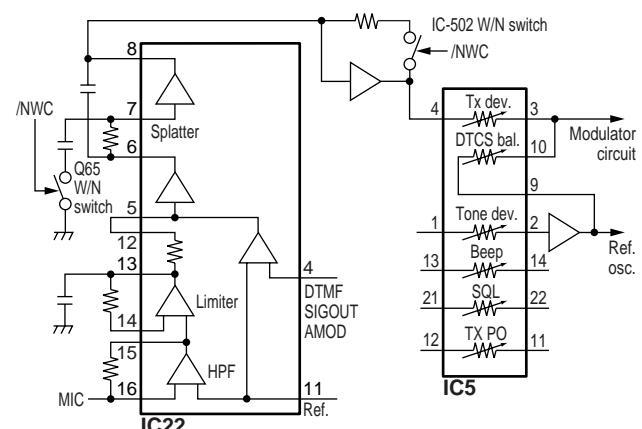
AF signals (MIC) from the FRONT unit via J5 (pin 10) are applied to the audio switch (IC25). While transmitting, the MCON signal from the CPU is high and the AF signals are passed through IC25 to the microphone amplifier circuit.

The AF signals from IC25 are applied to the microphone amplifier (IC22) via the pre-amplifier (IC21b). The amplified signals are applied to the limiter amplifier in IC22.

The entered signals are pre-emphasized with $+6\text{dB/octave}$ at a limiter amplifier, then passed through a splatter filter section in IC22. The output signals from pin 8 pass through the level controller (IC5, pins 4, 3) and are then applied to the modulation circuit (D46).

The N/W switches (Q65, IC502) are connected to the input lines of the splatter filter section (IC22) and buffer amplifier (IC503) respectively. The N/W switches (Q65, IC502) are controlled by the /NWC signal from Q505 to adjust filter cut-off frequency (by Q65) and maximum frequency deviation (by IC502).

• MICROPHONE AMPLIFIER CIRCUIT



4-2-2 MODULATION CIRCUIT

The modulation circuit modulates the VCO oscillating signal (RF signal) using the microphone AF signals.

The AF signals from the level controller (IC5) change the reactance of varactor diode (D46) to modulate the oscillated signal at the Tx VCO circuit (Q23). The modulated VCO signal is amplified at the buffer amplifiers (Q19, Q20) and is then applied to the drive amplifier circuit via the T/R switch (D17).

The CTCSS/DTCS signals from the CPU (IC20, pin 44) are amplified at the buffer amplifier (Q504). The amplified signals pass through the level controller (IC5, pins 1, 2) and are then applied to VCO circuit via the low-pass filter (IC21a).

When /NWC signal which is applied to N/W switch (Q64) is high level, N/W switch (Q64) changes the input level of the level controller (IC5), thus narrowing the bandwidth.

4-2-3 DRIVE/POWER AMPLIFIER CIRCUITS

The amplifier circuit amplifies the VCO oscillating signal to an output power level.

The signal from the buffer amplifier (Q20) passes through the T/R switch (D17), and is amplified at the drive amplifiers (Q17–Q15) and power module (IC11) to obtain 25 W of RF power.

The amplified signal is passed through the antenna switching circuit (D4), low-pass filter and APC detector. Then the signal is applied to the antenna connector.

The collector voltages for driver (Q16) come from the MT8V regulator (Q38, D28). The transmit mute switch (Q39) controls the MT8V regulator when transmit mute is necessary.

4-2-4 APC CIRCUIT

The APC circuit protects the power module (IC11) from a mismatched output load and stabilizes the output power.

The APC detector circuit detects forward signals and reflection signals at D3 and D1 respectively. The combined voltage is at a minimum level when the antenna impedance is matched at $50\ \Omega$ and is increased when it is mismatched.

The detected voltage is applied to the inverse amplifier (IC10b, pin 5), and the power setting voltage (PSET) is applied to the other input (IC10b, pin 6) via the amplifier (IC10a). When antenna impedance is mismatched, the detected voltage exceeds the power setting voltage. Then the output voltage of the inverse amplifier (IC10b, pin 7) controls the input current of the power module (IC11) to reduce the output power via the APC driver (Q11).

4-3 PLL CIRCUITS

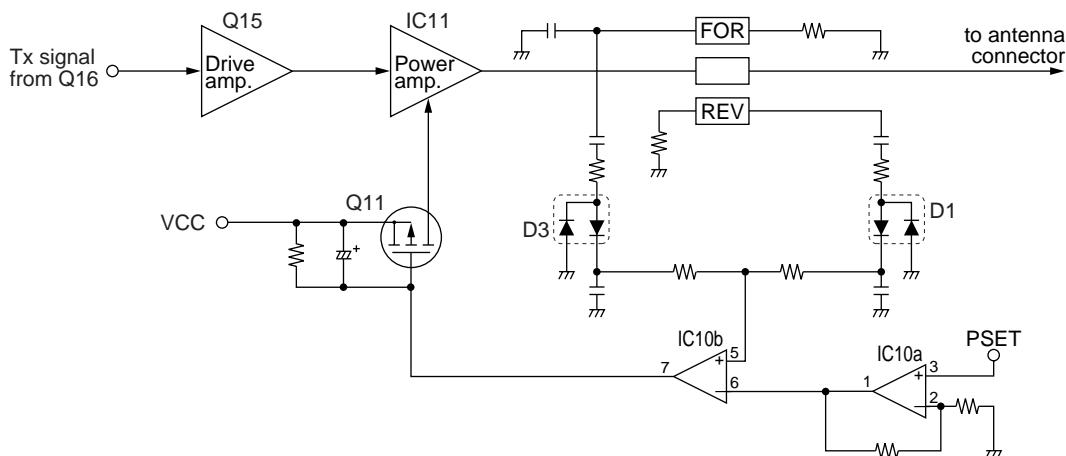
4-3-1 PLL CIRCUIT

A PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL circuit consists of the PLL IC, charge pump, loop filter and reference oscillator and employs a pulse swallow counter.

Oscillated signals from the VCO via the buffer amplifiers (Q19, Q18) are prescaled in the PLL IC (IC12, pin 11) based on the divided ratio (N-data). The PLL IC detects the out-of-step phase using the reference frequency and outputs it from pin 6 (IC12). The output signal is passed through the charge pump (Q30–Q33) and loop filters (R154/C181, R153/C179), and is then applied to the VCO circuit as the lock voltage.

The accelerator switch (IC13) selects the effective loop filter to accelerate the PLL lock up speed.

• APC CIRCUIT



The lock voltage is also used for the receiver tunable band-pass filters to match the filter's center frequency to the desired receive frequency. The lock voltage is amplified at the buffer amplifier (Q29) and is then applied to the band-pass filters (D7–D11, D514).

4-3-2 VCO CIRCUIT

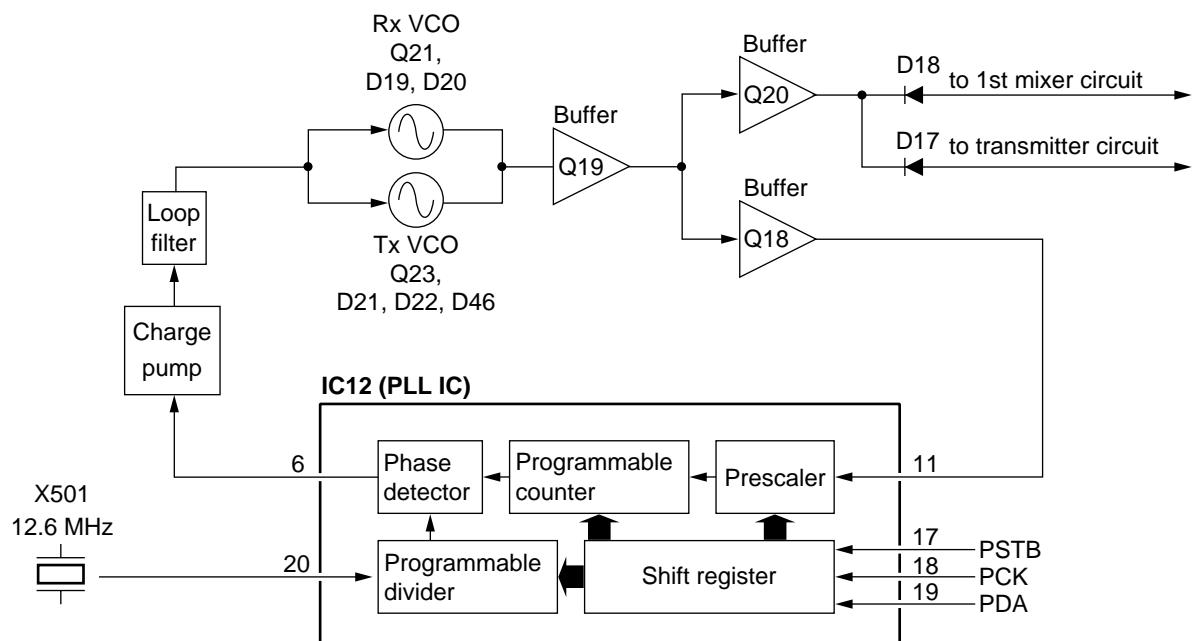
The VCO circuit contains a separate Rx VCO (Q21, D19, D20) and Tx VCO (Q23, D21, D22, D46). The oscillated signal is amplified at the buffer amplifiers (Q19, Q20) and is then applied to the T/R switches (D17, D18). Then the receive 1st LO (Rx) signal is applied to the 1st mixer (Q2) via the LO amplifier (Q3) and the transmit (Tx) signal to the driver (Q17).

A portion of the signal from the buffer amplifier (Q19) is fed back to the PLL IC (IC12, pin 11) via the another buffer amplifier (Q18) as the comparison signal.

4-4 POWER SUPPLY CIRCUIT VOLTAGE LINE

LINE	DESCRIPTION
HV	The voltage from the external power connector.
VCC	Same voltage as the HV line passed through the power control circuit (Q12, Q14) controlled by PWON signal from the CPU (IC20, pin 77).
CPU5V	Common 5 V converted from the HV line at the 5V regulator circuit (IC17). This voltage is supplied to the CPU regardless of the power switch.
+5V	Common 5 V converted from the VCC line at the +5V regulator circuit (Q42, Q43, D30) using the CPU5V line voltage as the reference.
+8V	Common 8 V converted from the VCC line at the +8V regulator circuit (IC16).
R8V	Receive 8 V converted from the VCC line at the R8V regulator circuit (Q36, D27) using the +8V line voltage as the reference and controlled by VRX signal from the CPU (IC20, pin 76).
T8V	Transmit 8 V converted from the VCC line at the T8V regulator circuit (Q40, D29) using the +8V line voltage as the reference and controlled by VTX signal from the the CPU (IC20, pin 75).
MT8V	Transmit 8 V converted from the VCC line at the MT8V regulator circuit (Q38, D28) using the +8V line voltage as the reference and controlled by TMUT signal from the the CPU (IC20, pin 62).

- **PLL circuit**



4-5 PORT ALLOCATIONS

CPU (IC20)

Pin number	Port name	Description
20	PTTO	Outputs the PTT control signal. Low : While transmitting
21	PTTI	Input port for the PTT control signal from PTTO port.
22	AFON	Input port for the AF amplifier ON signal from an optional unit.
24	BUSY	Outputs busy signal for an optional unit.
25	POSW	Input port for the power switch. Low : While power switch is pushed
30	MMUT	Input port for microphone audio mute control signal from an optional unit.
31	RMUT	Input port for receive audio mute control signal from an optional unit.
32	NOIS	Input port for noise signals (pulse-type) for noise squelch operation.
38	AFV	Input port for the volume control.
40	RSSI	Input port for receiving signal strength level detection.
43	CDEC	Input port for CTCSS/DTCS decoding.
44	CENC	Output ports for CTCSS/DTCS signals.
47, 48	ECS2, ECS1	Output ports for EEPROM select signals. ECS1: For internal EEPROM (IC27) ECS2: For optional EEPROM
49	ECK	Outputs clock signal for EEPROMs.
50	ESI	Input port for serial signal from EEPROMs.
51	ESO	Outputs serial signal for EEPROMs.
53	BEEP	Outputs beep audio signals.
55	MCON	Outputs mic. audio mute control signal to the audio switch (IC25). High : While DTMF signals are being transmitted, etc.
56	AMUT	Outputs the AF mute switch (Q6) control signal. High : While squelched, etc.
25	NWC	Outputs N/W switch control signals. High : While wide is selected
57	HFSW	Outputs high-pass filter's characteristics select signal. High : During CTCSS operation
60	PA	Outputs mic. audio select signal to the audio switch (IC25). High : While "Public-address" function is ON
62	TMUT	Outputs MT8V regulator circuit (Q38, D27) control signal. High : While transmit is muted.

Pin number	Port name	Description
64	DSTB	Outputs strobe signals for the level controller. (IC5)
65	DDA	Outputs data signal for the level controller (IC5).
66	DCK	Outputs clock signal for the level controller (IC5).
67	PSTB	Outputs strobe signals for the PLL IC (IC12).
68	PDA	Outputs data signal for the PLL IC (IC12).
69	PCK	Outputs clock signal for the PLL IC (IC12).
72	UNLK	Input port for the PLL unlock signal. High : During unlock
73	PLLT	Outputs PLL accelerator control signal. High : While scanning, etc.
75	VTX	Outputs the T8V regulator circuit (Q38, D28) control signal. Low : While transmitting
76	VRX	Outputs the R8V regulator circuit (Q36, D27) control signal. Low : While receiving
77	PWON	Outputs the power control circuit (Q12) control signal. High : During power ON
78	PASP	Outputs "Public-address" mute signal. High : While PA and Ext. SP functions are not used
79	SP	Outputs the mute switch (Q7) control signal (incl. beep). High : While squelched, etc.
80	DIM	Input port for an external LCD backlight brightness control signal. Low : LCD backlight is dimmed
82-89	DTR1– DTR4, DTC4– DTC1	Outputs DTMF audio signals.
93	HORN	Outputs high level control signal for the pre-set time to the connected external unit when matched 2- or 5-tone code is received.
99	SIFT	Outputs CPU clock shift signal.

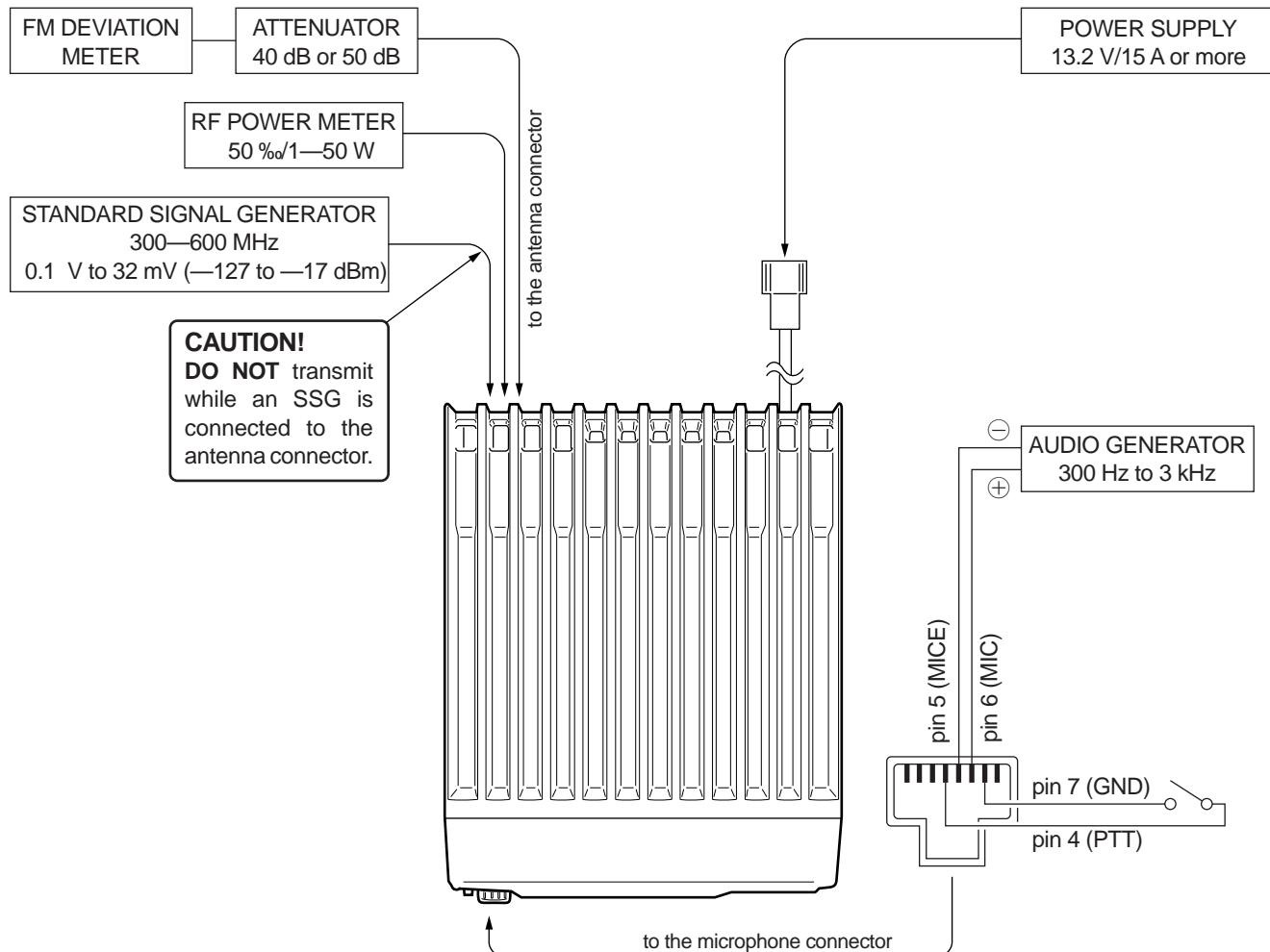
SECTION 5 ADJUSTMENT PROCEDURES

5-1 PREPARATION

■ REQUIRED TEST EQUIPMENT

EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output Voltage : 13.2 V DC Current capacity : 15 A or more	Standard signal generator (SSG)	Frequency range : 0.1–600 MHz Output level : 0.1 μV–32 mV (-127 to -17 dBm)
RF power meter (terminated type)	Measuring range : 1–50 W Frequency range : 300–600 MHz Impedance : 50 Ω SWR : Less than 1.2 : 1	Oscilloscope	Frequency range : DC–20 MHz Measuring range : 0.01–20 V
Frequency counter	Frequency range : 0.1–600 MHz Frequency accuracy : ±1 ppm or better Sensitivity : 100 mV or better	DC voltmeter	Input impedance : 50 kΩ/V DC or better
RF voltmeter	Frequency range : 0.1–600 MHz Measuring range : 0.01–10 V	Digital multimeter	Measuring range : 10 mV–10 V
FM deviation meter	Frequency range : DC–600 MHz Measuring range : 0 to ±10 kHz	AC millivoltmeter	Input impedance : 10 MΩ/V DC or better
Audio generator	Frequency range : 300–3000 Hz Measuring range : 1–500 mV	External speaker	Input impedance : 4 Ω Capacity : 5 W or more
		Attenuator	Power attenuation : 40 dB or 50 dB Capacity : 50 W or more
		Terminator	Impedance : 50 Ω Capacity : 50 W or more

■ CONNECTION



5-2 INITIAL SET MODE

The following items can be adjusted via ***Initial Set Mode*** without opening the transceiver's case.

■ ENTERING INITIAL SET MODE

- ① Turn the transceiver power OFF.
- ② While pushing **[△]** and **[▽]**, turn power ON.
• Bank number appears regardless of the channel separation type, 'bank' or 'free'.
- ③ Push **[P0]** to cycle through the initial set mode items.

NOTE: Initial Set Mode access can be inhibited through PC programming. In such case, **[P0]** cannot be used and only 'DISPLAY' setting is available. Ask your Dealer or Icom Service Center for PC programming.

■ SELECTABLE ITEMS

No.	ITEM	SELECTABLE CONDITIONS		
		[P1]	[P2]	[P3]
1	DISPLAY	Backlight	—	Contrast
2	AF/SQUELCH	—	Squelch level	Minimum AF level
3	BEEP TONES	Link ON/OFF	Beep tones ON/OFF	Beep level
4	DEVIATION	DTCS balance	Tone deviation	Max. voice deviation
5	S-METER	Indicate the received signal level regardless of [P1] to [P3] keys.		
6	TX POWER	Low 1	Low 2	High

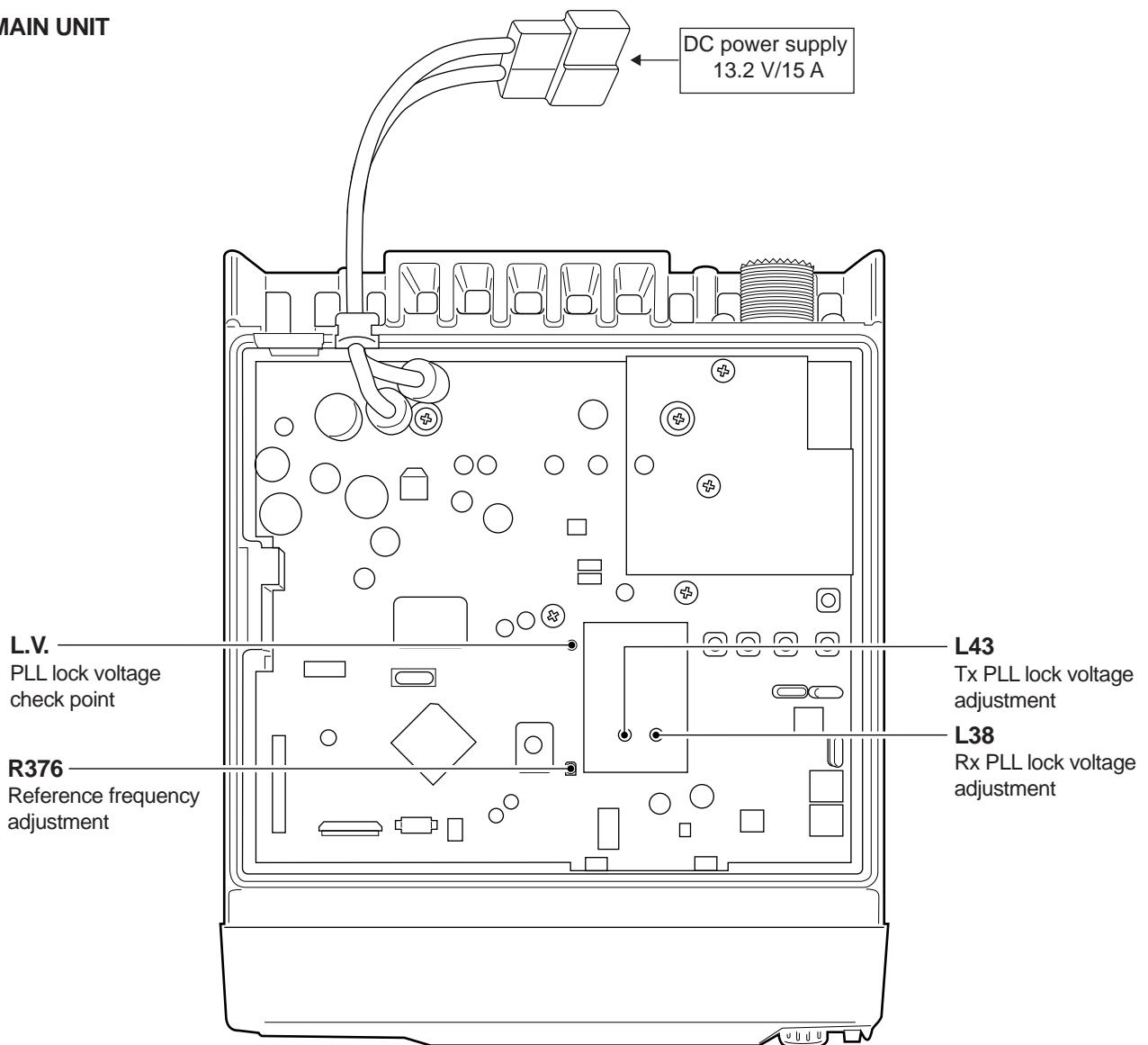
■ SELECTION METHOD

MODE No.	ITEM	METHOD	NOTE
[1]	Display backlight	Push [P1] to select 'bright', 'dark' or 'off'.	
	Display contrast	Rotate the volume control while pushing [P3] .	
[2]	Squelch level	Rotate the volume control while pushing [P2] .	
	Minimum AF level	Rotate the volume control while pushing [P3] .	
[3]	Link/unlink beep tones with the volume control	Push [P1] to select 'link' or 'unlink'.	2 beeps: link 1 beep: unlink
	Beep ON/OFF	Push [P2] to turn beeps ON and OFF.	2 beeps: ON 1 beep: OFF Effective after exiting Initial Set Mode
	Maximum beep level	Push [P3] to select the desired beep level.	
[4]	DTCS balance	Rotate the volume control while pushing [P1] .	Automatic transmission while pushing the key.
	CTCSS/DTCS Tone deviation	Rotate the volume control while pushing [P2] .	Automatic transmission while pushing the key. Separate setting for CTCSS and DTCS depending on the programmed tone system.
	Maximum voice deviation	Rotate the volume control while pushing [P3] .	Automatic transmission while pushing the key.
[5]	S-meter level	—	Received signal level is shown in the display.
[6]	Transmit low power (L1)	Rotate the volume control while pushing [P1] .	Automatic transmission while pushing the key.
	Transmit low power (L2)	Rotate the volume control while pushing [P2] .	Same as above.
	Transmit high power (H)	Rotate the volume control while pushing [P3] .	Same as above.

5-3 PLL ADJUSTMENT

ADJUSTMENT		ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
			UNIT	LOCATION		UNIT	ADJUST
PLL LOCK VOLTAGE	1	• Operating frequency : 440.000 MHz • Receiving	MAIN	Connect a digital multimeter or an oscilloscope to the check point "LV".	2.0 V	MAIN	L38
	2	• Operating frequency : 490.000 MHz • Receiving			7.0–13.0 V		Verify
	3	• Operating frequency : 440.000 MHz • Transmitting			2.0 V		L43
	4	• Operating frequency : 490.000 MHz • Transmitting			7.0–13.0 V		Verify
PLL REFERENCE FREQUENCY	1	• Operating frequency : 440.000 MHz • Transmitting	Rear panel	Loosely couple a frequency counter to the antenna connector.	490.0000 MHz	MAIN	R376

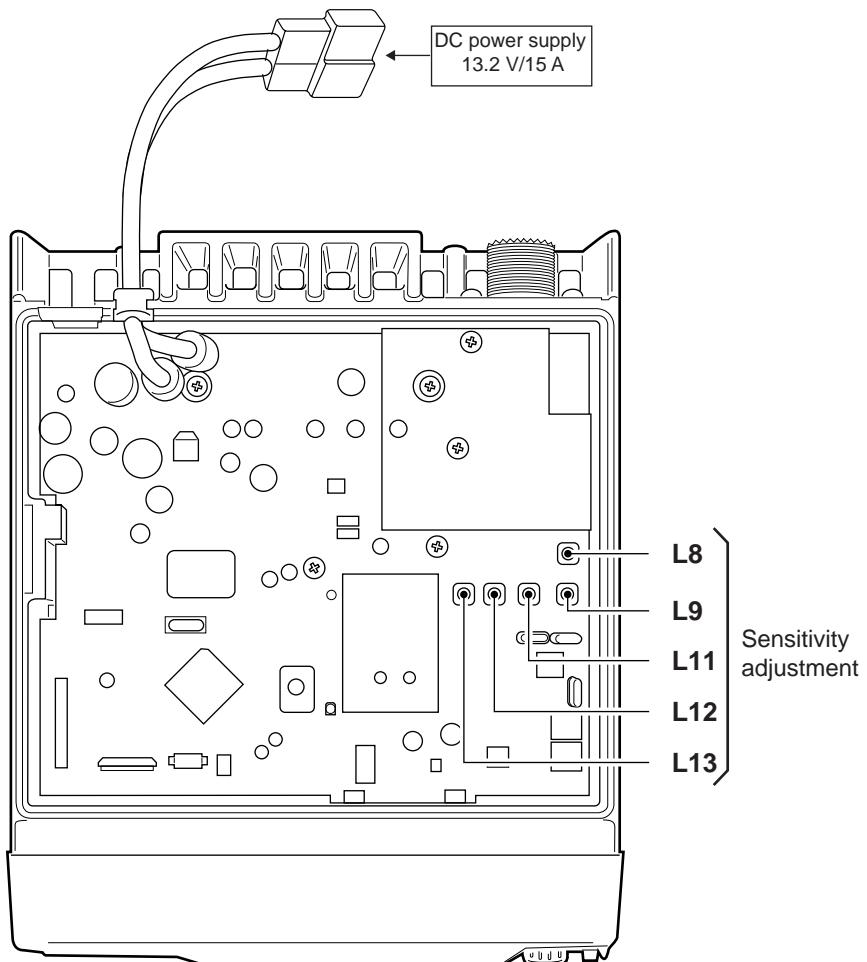
• MAIN UNIT



5-4 RECEIVER ADJUSTMENT

ADJUSTMENT	ADJUSTMENT CONDITIONS	MEASUREMENT METHOD	ADJUSTMENT	
			VALUE	ADJUST
RECEIVER SENSITIVITY	1 • Operating frequency : 440.000 MHz • Enter Initial Set Mode No. 5. Refer to page 5-2 for details. • Connect a standard signal generator to the antenna connector and set as: Level : 5.6 μ V* (-92 dBm) Modulation : OFF • Connect a 4 Ω load to the external speaker jack. • Receiving	Number digits in the LCD 	Maximum indication	Adjust in sequence L8, L9, L11, L12, L13 on the MAIN unit.
NOISE SQUELCH THRESHOLD POINT	1 • Operating frequency : 136.000 MHz • Enter Initial Set Mode No. 2. Refer to page 5-2 for details. • Make sure no signal is being applied to the antenna connector. • Receiving	Speaker output	At the point where the noise audio just disappears.	Rotate volume control while pushing P2 .
	2 • Connect an SSG to the antenna connector and set as: Level : 0.32 μ V* (-117 dBm) Deviation : \pm 3.5 kHz Modulation : 1 kHz • Receiving		Squelch opens.	Verify
BEEP LEVEL	1 • Operating frequency : Any • Enter Initial Set Mode No. 3. Refer to page 5-2 for details. • Receiving	Speaker output	Desired level	Push P3 to select the beep level.

*This output level of the standard signal generator (SSG) is indicated as SSG's open circuit.



5-5 TRANSMITTER ADJUSTMENT

ADJUSTMENT		ADJUSTMENT CONDITIONS	MEASUREMENT METHOD	ADJUSTMENT	
				VALUE	ADJUST
OUTPUT POWER	1	<ul style="list-style-type: none"> Operating frequency : 440.000 MHz Enter Initial Set Mode No. 6. Refer to page 5-2 for details. 	Connect an RF power meter to the antenna connector.	25 W	Rotate volume control while pushing P3 .
				10 W	Rotate volume control while pushing P2 .
				2.5 W	Rotate volume control while pushing P1 .
NOTE: When the RF output power cannot be set with this procedure, cloning may be necessary to cancel the output power setting.					
FM DEVIATION	1	<ul style="list-style-type: none"> Operating frequency : 440.000 MHz Enter Initial Set Mode No. 4. Refer to page 5-2 for details. Connect an audio generator to the microphone connector and set as: 1 kHz/35 mV Set an FM deviation meter as: HPF : OFF LPF : 20 kHz De-emphasis: OFF Detector : (P-P)/2 Wide/Narrow setting : Wide 	Connect an FM deviation meter to the antenna connector through an attenuator.	±4.0 kHz	Rotate volume control while pushing P3 .
				±1.75–±2.2 kHz	Verify
	2	Wide/Narrow setting : Narrow			
DTCS WAVE FORM AND DEVIATION	1	<ul style="list-style-type: none"> Operating frequency : 465.000 MHz Enter Initial Set Mode No. 4. Refer to page 5-2 for details. No AF signals are applied to the microphone connector. Set an FM deviation meter as: HPF : OFF LPF : 20 kHz De-emphasis: OFF Detector : (P-P)/2 Wide/Narrow setting : Wide DTCS code : 007 	Connect an FM deviation meter with an oscilloscope to the antenna connector through an attenuator.	Set to flat wave form	Rotate volume control while pushing P1 .
				±0.7 kHz	Rotate volume control while pushing P2 .
	3	Wide/Narrow setting : Narrow		±0.25–±0.5 kHz	Verify
CTCSS TONE DEVIATION	1	<ul style="list-style-type: none"> Operating frequency : 465.000 MHz Enter Initial Set Mode No. 4. Refer to page 5-2 for details. No AF signals are applied to the microphone connector. Set an FM deviation meter as: HPF : OFF LPF : 20 kHz De-emphasis: OFF Detector : (P-P)/2 Wide/Narrow setting : Wide CTCSS tone frequency: 88.5 Hz 	Connect an FM deviation meter to the antenna connector through an attenuator.	±0.7 kHz	Rotate volume control while pushing P2 .
	2	Wide/Narrow setting : Narrow		±0.25–±0.5 kHz	Verify

SECTION 6 PARTS LIST

[FRONT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
IC1	1130009120	S.IC	SED1526F0A
IC2	1140007631	S.IC	HD6433640A33H
IC3	1110003500	S.IC	S-80742SL-A6-T1
IC4	1130005720	S.IC	TC7W04F (TE12L)
IC5	1110003390	S.IC	AN8005M-(E1)
Q1	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q2	1590001330	S.TRANSISTOR	DTA114EUA T106
Q3	1590000680	S.TRANSISTOR	DTC114EUA T106
Q4	1590001330	S.TRANSISTOR	DTA114EUA T106
Q5	1590000680	S.TRANSISTOR	DTC114EUA T106
Q6	1590000440	S.TRANSISTOR	DTA143ZUA T106
Q7	1590000440	S.TRANSISTOR	DTA143ZUA T106
Q8	1590000440	S.TRANSISTOR	DTA143ZUA T106
Q9	1590000440	S.TRANSISTOR	DTA143ZUA T106
Q10	1590000680	S.TRANSISTOR	DTC114EUA T106
Q11	1590000680	S.TRANSISTOR	DTC114EUA T106
D1	1790001280	S.DIODE	MA111 (TX)
D2	1790001280	S.DIODE	MA111 (TX)
D3	1790001280	S.DIODE	MA111 (TX)
D4	1790001280	S.DIODE	MA111 (TX)
D5	1750000130	S.DIODE	DA204U T107
D6	1750000130	S.DIODE	DA204U T107
D7	1750000130	S.DIODE	DA204U T107
D8	1750000130	S.DIODE	DA204U T107
D9	1750000130	S.DIODE	DA204U T107
X1	6050009870	S.XTAL	CR-567 (9.8304 MHz)
L1	6200001720	S.COIL	NL 322522T-1R0J
R1	7030003810	S.RESISTOR	ERJ3GEYJ 125 V (1.2 MΩ)
R2	7310002820	S.TRIMMER	RV-158 (RH03A3AS5) 474
R3	7030003800	S.RESISTOR	ERJ3GEYJ 105 V (1 MΩ)
R4	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R5	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R6	7030003760	S.RESISTOR	ERJ3GEYJ 474 V (470 kΩ)
R7	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R8	7030003720	S.RESISTOR	ERJ3GEYJ 224 V (220 kΩ)
R9	7030003720	S.RESISTOR	ERJ3GEYJ 224 V (220 kΩ)
R10	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R11	7030003800	S.RESISTOR	ERJ3GEYJ 105 V (1 MΩ)
R12	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R13	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R15	7030003560	S.RESISTOR	ERJ3GEYJ 103 V (10 kΩ)
R16	7030003500	S.RESISTOR	ERJ3GEYJ 332 V (3.3 kΩ)
R17	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R18	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R19	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R20	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R21	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R22	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R23	7030003500	S.RESISTOR	ERJ3GEYJ 332 V (3.3 kΩ)
R24	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R25	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R26	7210002830	VARIABLE	EVU-F2JFK4 B14
R27	7030003340	S.RESISTOR	ERJ3GEYJ 151 V (150 Ω)
R28	7030003340	S.RESISTOR	ERJ3GEYJ 151 V (150 Ω)
R29	7030003370	S.RESISTOR	ERJ3GEYJ 271 V (270 Ω)
R30	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R31	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R32	7030003390	S.RESISTOR	ERJ3GEYJ 391 V (390 Ω)
R34	7030003360	S.RESISTOR	ERJ3GEYJ 221 V (220 Ω)
R35	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R36	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R37	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R38	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R39	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R40	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R41	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)

[FRONT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
R42	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R43	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R44	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R45	7030003680	S.RESISTOR	ERJ3GEYJ 104 V (100 kΩ)
R46	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R47	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R48	7030003440	S.RESISTOR	ERJ3GEYJ 102 V (1 kΩ)
R49	7030003600	S.RESISTOR	ERJ3GEYJ 223 V (22 kΩ)
C1	4030011600	S.CERAMIC	C1608 JB 1C 104KT-N
C2	4030011600	S.CERAMIC	C1608 JB 1C 104KT-N
C3	4030011600	S.CERAMIC	C1608 JB 1C 104KT-N
C4	4030011600	S.CERAMIC	C1608 JB 1C 104KT-N
C5	4030011600	S.CERAMIC	C1608 JB 1C 104KT-N
C6	4030012600	S.CERAMIC	C2012 JB 1A 105M-T-A
C7	4030012600	S.CERAMIC	C2012 JB 1A 105M-T-A
C8	4030008630	S.CERAMIC	C1608 JF 1C 104Z-T-A
C9	4030006860	S.CERAMIC	C1608 JB 1H 102K-T-A
C10	4030009000	S.CERAMIC	C2012 JB 1C 224K-T-A
C11	4030006900	S.CERAMIC	C1608 JB 1E 103K-T-A
C13	4030007030	S.CERAMIC	C1608 CH 1H 150J-T-A
C14	4030007030	S.CERAMIC	C1608 CH 1H 150J-T-A
C15	4030006860	S.CERAMIC	C1608 JB 1H 102K-T-A
C16	4030006900	S.CERAMIC	C1608 JB 1E 103K-T-A
C17	4030008630	S.CERAMIC	C1608 JF 1C 104Z-T-A
C18	4030008630	S.CERAMIC	C1608 JF 1C 104Z-T-A
C19	4030006850	S.CERAMIC	C1608 JB 1H 471K-T-A
C20	4550006250	S.TANTALUM	TEMSVA 1A 106M-8L
C21	4550003220	S.TANTALUM	TEMSVA 1E 105M-8L
C22	4030008630	S.CERAMIC	C1608 JF 1C 104Z-T-A
C23	4030006850	S.CERAMIC	C1608 JB 1H 471K-T-A
C24	4030006850	S.CERAMIC	C1608 JB 1H 471K-T-A
C25	4030006850	S.CERAMIC	C1608 JB 1H 102K-T-A
C26	4030008630	S.CERAMIC	C1608 JF 1C 104Z-T-A
C27	4030006860	S.CERAMIC	C1608 JB 1H 102K-T-A
C28	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C29	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C30	4030006860	S.CERAMIC	C1608 JB 1H 102K-T-A
C31	4030006860	S.CERAMIC	C1608 JB 1H 102K-T-A
C32	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C33	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C34	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C35	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C36	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C37	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C38	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C39	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C40	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C41	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C42	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C43	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C44	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C45	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C46	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C47	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C48	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C49	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C50	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C51	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
C52	4030007090	S.CERAMIC	C1608 CH 1H 470J-T-A
J1	6450001470	CONNECTOR	95003-2881
J2	6510018030	S.CONNECTOR	53248-1217
DS1	5030001650	LCD	LD-NU10377E
DS2	5040002470	S.LED	FY1112H
DS3	5040002470	S.LED	FY1112H
DS4	5040002470	S.LED	FY1112H
DS5	5040002470	S.LED	FY1112H
DS6	5040002470	S.LED	FY1112H
DS7	5040002030	S.LED	CL-170Y-CD-T
DS8	5040002030	S.LED	CL-170Y-CD-T
DS10	5040002030	S.LED	CL-170Y-CD-T

S.=Surface mount

[FRONT UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
DS11	5040002030	S.LED	CL-170Y-CD-T
DS13	5040002030	S.LED	CL-170Y-CD-T
DS14	5040002030	S.LED	CL-170Y-CD-T
DS15	5040002030	S.LED	CL-170Y-CD-T
DS16	5040002030	S.LED	CL-170Y-CD-T
W1	7030003860	S.JUMPER	ERJ3GE JPW V
W2	7030003860	S.JUMPER	ERJ3GE JPW V
W3	7030000010	S.JUMPER	MCR10EZHJ JPW (000)
W4	7030000010	S.JUMPER	MCR10EZHJ JPW (000)
EP1	0910050602	PCB	B 5205B
EP2	8930048320	LCD CONTACT	SRDN-2140-SP-N-W

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	
Q31	1590002300	S.TRANSISTOR	FMW2 T148
Q32	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q33	1510000510	S.TRANSISTOR	2SA1576A T106R
Q35	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q36	1540000550	S.TRANSISTOR	2SD1664 T100Q
Q37	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q38	1540000550	S.TRANSISTOR	2SD1664 T100Q
Q39	1590000850	S.TRANSISTOR	DTC114YUA T106
Q40	1540000550	S.TRANSISTOR	2SD1664 T100Q
Q41	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q42	1520000560	S.TRANSISTOR	2SB1123T-TD
Q43	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q44	1560000810	S.FET	2SK1069-4-TL
Q45	1590000430	S.TRANSISTOR	DTC144EUA T106
Q47	1590000430	S.TRANSISTOR	DTC144EUA T106
Q48	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q50	1590001330	S.TRANSISTOR	DTA114EUA T106
Q51	1590000680	S.TRANSISTOR	DTC114EUA T106
Q52	1560000810	S.FET	2SK1069-4-TL
Q55	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q56	1590000720	S.TRANSISTOR	DTA144EUA T106
Q61	1530002850	S.TRANSISTOR	2SC4116-BL (TE85R)
Q62	1590000430	S.TRANSISTOR	DTC144EUA T106
Q64	1590000430	S.TRANSISTOR	DTC144EUA T106
Q65	1560000840	S.FET	2SK1829 (TE85R)
Q501	1590000430	S.TRANSISTOR	DTC144EUA T106
Q502	1590000720	S.TRANSISTOR	DTA144EUA T106
Q503	15900001650	S.TRANSISTOR	XP4601 (TX)
Q504	1560000810	S.FET	2SK1069-4-TL
Q505	1590000430	S.TRANSISTOR	DTC144EUA T106
Q506	1560000810	S.FET	2SK1069-4-TL
Q507	1590000430	S.TRANSISTOR	DTC144EUA T106
Q508	1560000840	S.FET	2SK1829 (TE85R)
Q509	1590000430	S.TRANSISTOR	DTC144EUA T106
Q510	1590000430	S.TRANSISTOR	DTC144EUA T106
Q511	1590000720	S.TRANSISTOR	DTA144EUA T106
Q512	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q513	1590000720	S.TRANSISTOR	DTA144EUA T106
Q514	1590000720	S.TRANSISTOR	DTA144EUA T106
Q515	1590000720	S.TRANSISTOR	DTA144EUA T106
Q516	1530002060	S.TRANSISTOR	2SC4081 T107 R
Q517	1590000430	S.TRANSISTOR	DTC144EUA T106
Q518	1590000680	S.TRANSISTOR	DTC114EUA T106
D1	1790000980	S.DIODE	MA742 (TX)
D3	1790000980	S.DIODE	MA742 (TX)
D4	1750000510	S.DIODE	UM9401F
D5	1710000730	S.DIODE	MI809-T11
D6	1710000730	S.DIODE	MI809-T11
D7	1720000370	S.VARICAP	HVU350TRF
D8	1720000370	S.VARICAP	HVU350TRF
D9	1720000370	S.VARICAP	HVU350TRF
D10	1720000370	S.VARICAP	HVU350TRF
D11	1720000370	S.VARICAP	HVU350TRF
D15	1790000700	DIODE	DSA3A1
D16	1750000370	S.DIODE	DA221 TL
D17	1790000620	S.DIODE	MA77 (TX)
D18	1790000620	S.DIODE	MA77 (TX)
D19	1720000270	S.VARICAP	1SV217 (TPH2)
D20	1720000270	S.VARICAP	1SV217 (TPH2)
D21	1720000270	S.VARICAP	1SV217 (TPH2)
D22	1720000270	S.VARICAP	1SV217 (TPH2)
D27	1750000130	S.DIODE	DA204U T107
D28	1750000130	S.DIODE	DA204U T107
D29	1750000130	S.DIODE	DA204U T107
D30	1750000550	S.DIODE	1SS355 TE-17
D31	1720000360	S.DIODE	HSU88TRF
D32	1790000980	S.DIODE	MA742 (TX)
D33	1730002420	S.ZENER	MA8160 (TX)
D36	1750000550	S.DIODE	1SS355 TE-17
D37	1750000550	S.DIODE	1SS355 TE-17
D38	1750000550	S.DIODE	1SS355 TE-17
D45	1790001280	S.DIODE	MA111 (TX)
D46	1720000520	S.VARICAP	1T365-01-T8A
D50	1750000550	S.DIODE	1SS355 TE-17
D51	1750000550	S.DIODE	1SS355 TE-17
D52	1750000260	S.DIODE	1SS352 (TPH3)
D501	1160000060	S.DIODE	DAN202U T107
D502	1160000060	S.DIODE	DAN202U T107
D503	1790000620	S.DIODE	MA77 (TX)
D505	1750000130	S.DIODE	DA204U T107

S.=Surface mount

SECTION 7 MECHANICAL PARTS

[CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J1	6510004880	Connector MR-DS-E 01	1
MP1	8010016320	1705 chassis	1
MP2	8810008660	Screw PH BT M3 X 8 NI-ZU	4
MP3	8810008660	Screw PH BT M3 X 8 NI-ZU	2
MP4	8810008660	Screw PH BT M3 X 8 NI-ZU	2
MP5	8810009130	Screw PH BT M3 X 12 NI-ZU	4
MP6	8930027480	1126 TR-A clip	1
MP7	8820000870	1705 cap screw	3
MP8	8110005570	1705 cover	1
MP11	8930039610	Thermally sheet (C)	1
MP15	8930039630	1706 jack sheet	1
MP16	8930036771	1705 main seal-1	1

[UNPACKING]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
F1	5210000120	Fuse FGB 15A	2
MC1	Optional product	Microphone HM-100	1
SP1	Optional product	Speaker SP-22	1
W1	Optional product	Cable OPC-345	1
W2	Optional product	Cable OPC-049	1
MP1	8010016730	150 mounting bracket	1
MP3	8820000530	Flang bolt M4 X 8 NI	4
MP4	8810000470	Screw PH M5 X 12 (+/-)	4
MP5	8810005840	Screw PH A M5 X 20	4
MP6	8850000150	Flat washer M5 NI BS	4
MP7	8850000390	Spring washer M5	4
MP8	8830000120	Nut M5	4
MP9	6910004210	731 MIC hanger set (ind. screw, washer)	1
MP10	8310045550	1705 LCD seal (C)	1

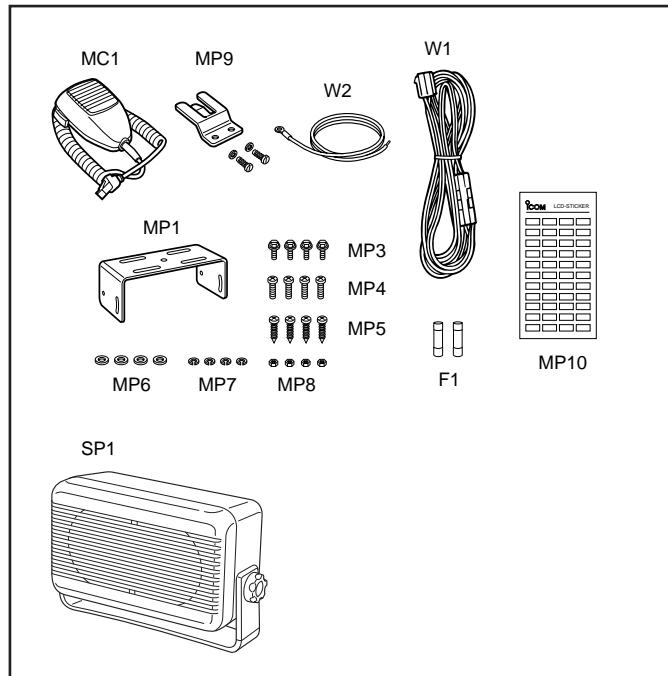
[FRONT UNIT]

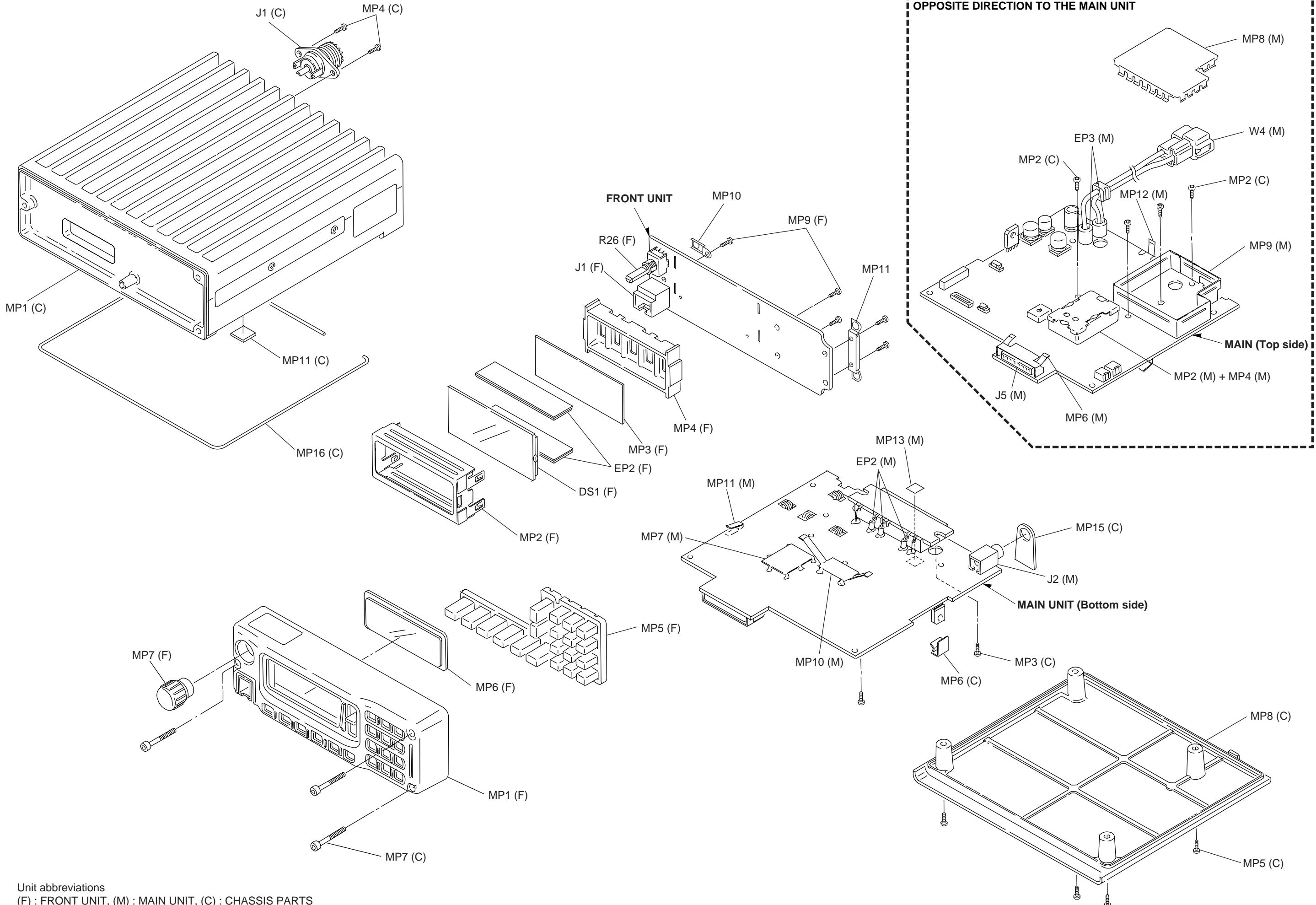
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
R26	7210002830	EVU-F2JFK4 B14	1
J1	6450001470	Connector 95003-2881	1
DS1	5030001650	LCD LD-NU10377E	1
EP2	8930048320	LCD contact SRCN-2140-SP-N-W	2
MP1	8210015740	2140 front panel	1
MP2	8930047980	2140 LCD holder	1
MP3	8930048290	2140 LCD filter	1
MP4	8210015770	2140 reflector	1
MP5	8930047860	2140 front key	1
MP6	8310094830	2140 window plate	1
MP7	8610009840	Knob N234	1
MP9	8810008760	Screw PH BT M2 X 8 NI-ZU	5
MP10	8930048910	2140 earth plate	1
MP11	8930049640	2141 plate	1

[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
J2	6450000140	Connector HSJ0807-01-010	1
J5	6510018040	Connector 52330-1217	1
W4	8900004540	Cable OPC-453	1
EP2	6910010220	Bead HF70 BB 3.5 X 5 X 1.3	3
EP3	6910010280	Bead HF70 BB 9.5 X 10.4 X 4.9	2
MP1	8510006810	DC-DC case	1
MP2	8510009980	1705 VCO case	1
MP4	8510010080	1705 VCO cover	1
MP6	8930037840	1705 connector spring	1
MP7	8510005070	599 shield plate	1
MP8	8510010240	1705 LPF cover	1
MP9	8510010230	1705 LPF case	1
MP10	8510010250	1705 shield plate	1
MP11	8930029511	1327 ANT plate-1	1
MP12	8930038790	1706 spring	1
MP13	8930049590	Copper sheet (G)	2

Screw abbreviations BT: Self-tapping PH: Pan head
NI: Nickel NI-ZU: Nickel-Zinc
BS: Brass





SECTION 8 SEMI-CONDUCTOR INFORMATION

• TRANSISTOR AND FET'S

2SA1576 R (Symbol: FR)	2SB1123T-TD (Symbol: BF)	2SC3356 R25-T2B (Symbol: R25)	2SC4081 T107 R (Symbol: BR)	2SC4116-BL (Symbol: LL)
2SC4226-T2 R25 (Symbol: R25)	2SC4703-T1 SE (Symbol: SE)	2SC5107-O (Symbol: MFO)	2SC5110-O (Symbol: MGO)	2SD1664 T100Q (Symbol: DA)
2SJ377 (Symbol: J377)	2SK1069-4-TL (Symbol: FJ)	2SK1829 (Symbol: KI)	2SK302-GR (Symbol: TG)	2SK880-GR (Symbol: XG)
3SK166A-2-T7 (Symbol: K)	DTA114EUA T106 (Symbol: 14)	DTA143ZUA T106 (Symbol: 113)	DTA144EUA T106 (Symbol: 16)	DTC114EUA T106 (Symbol: 24)
DTC114YUA T106 (Symbol: 64)	DTC144EUA T106 (Symbol: 26)	DTC363EK T147 (Symbol: H27)	FMS2A T148 (Symbol: S2)	FMW2 T148 (Symbol: W2)
XP4601 (Symbol: 5C)				

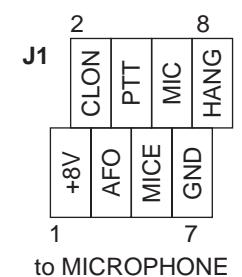
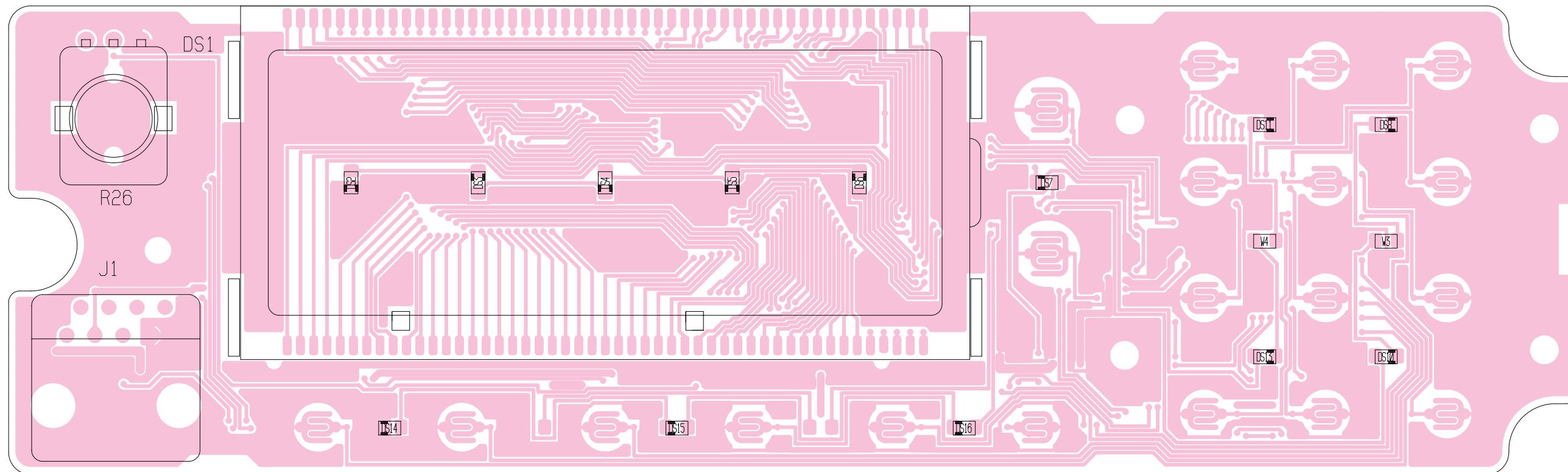
• DIODES

1SS302 (Symbol: C3)	1SS352 (Symbol: C1)	1SS355 (Symbol: A)	1SV217 (Symbol: T6)	1T365-01-T8A (Symbol: Pink)
DA204U T107 (Symbol: K)	DA221 TL (Symbol: K)	DAN202U T107 (Symbol: N)	HSU88TRF (Symbol: 9)	HVU350TRF (Symbol: 4)
MA11 (Symbol: 1B)	MA742 (Symbol: M1U)	MA77 (Symbol: 4B)	MA8160 (Symbol: 16^)	MI809-T11 (Symbol: Red dot)
UM9401F (No Symbol)				

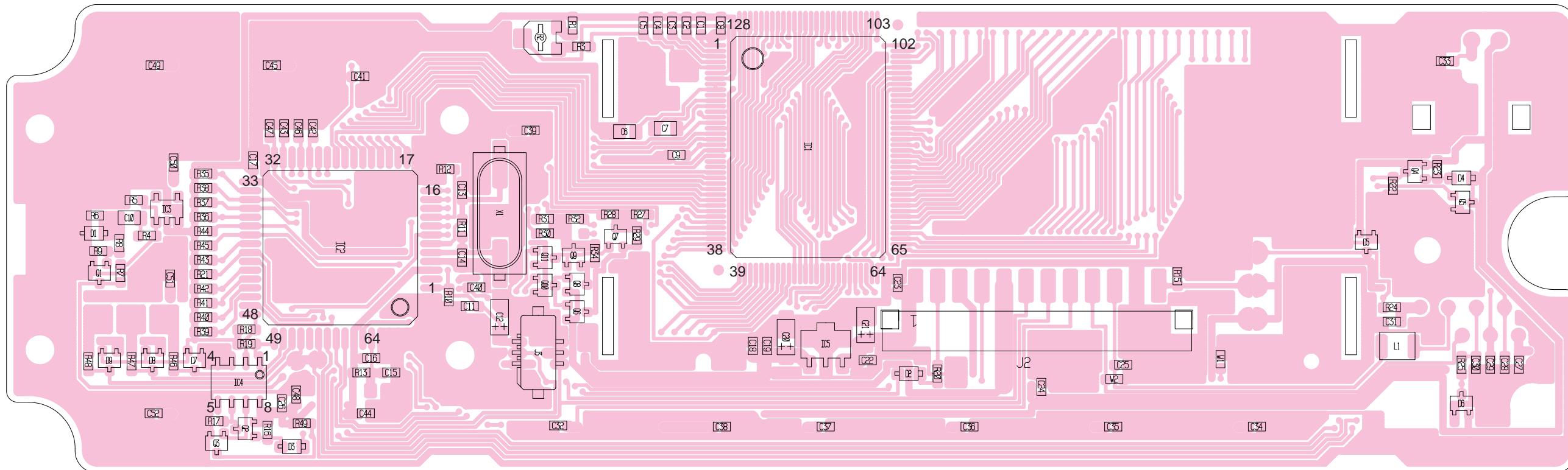
SECTION 9 BOARD LAYOUTS

9-1 FRONT UNIT

•TOP VIEW



● BOTTOM VIEW

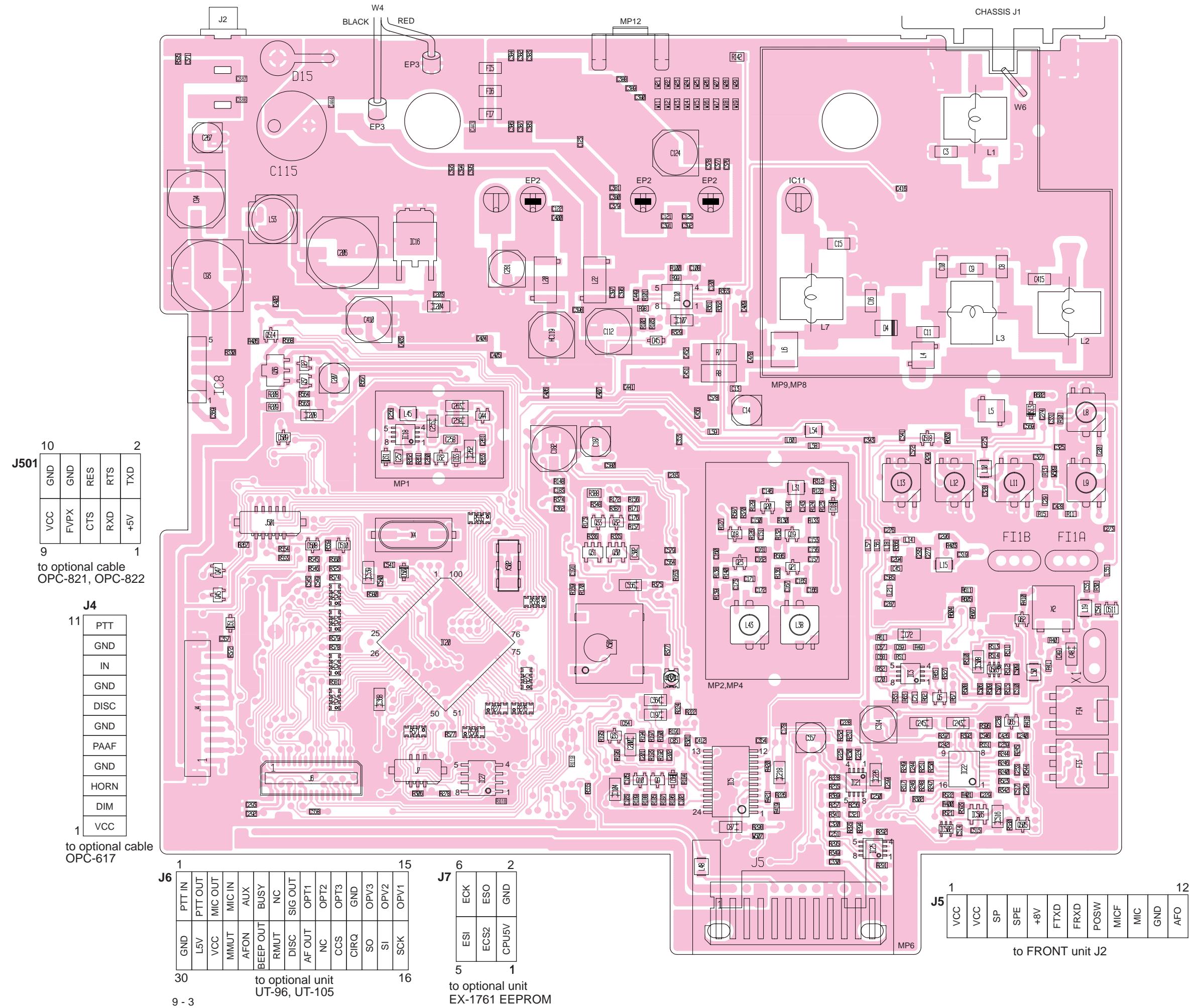


J2	1	12
	VCC	
	SQL	
	SP	
	SPE	
	+8V	
	FTXD	
	FRXD	
	POSW	
	MICE	
	MIC	
	GND	
	AFO	

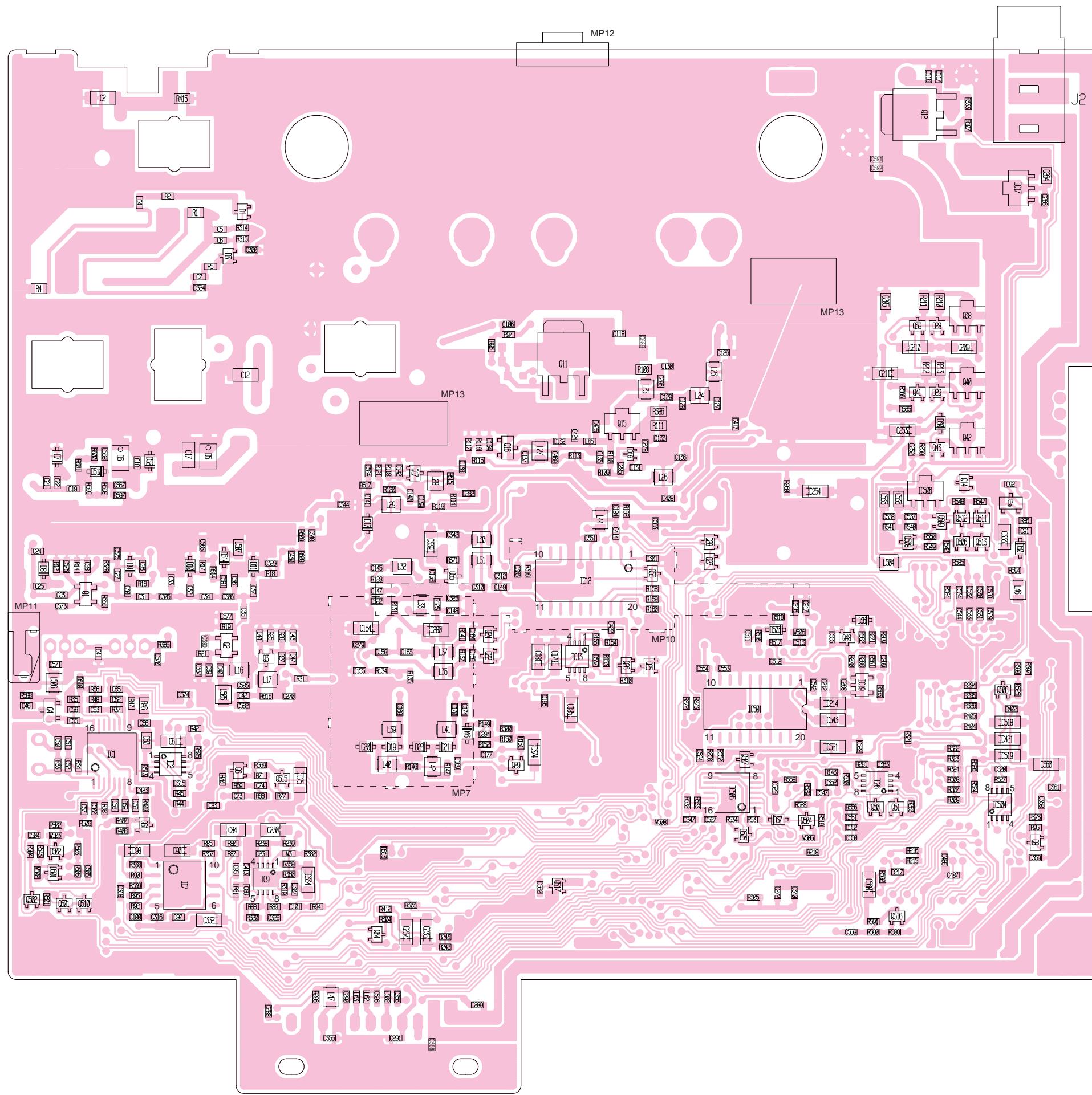
to MAIN unit J5

9-2 MAIN UNIT

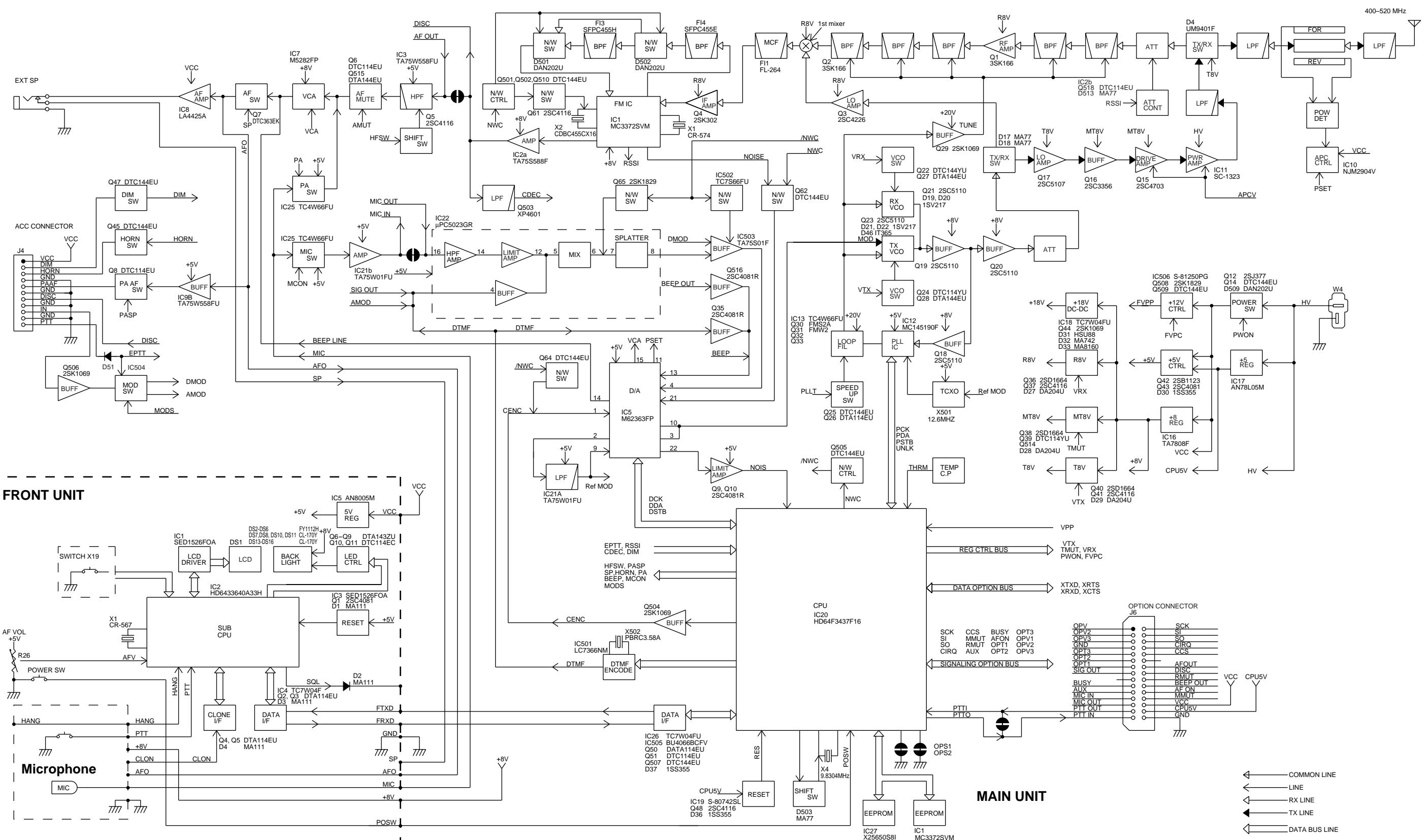
• TOP VIEW



● BOTTOM VIEW

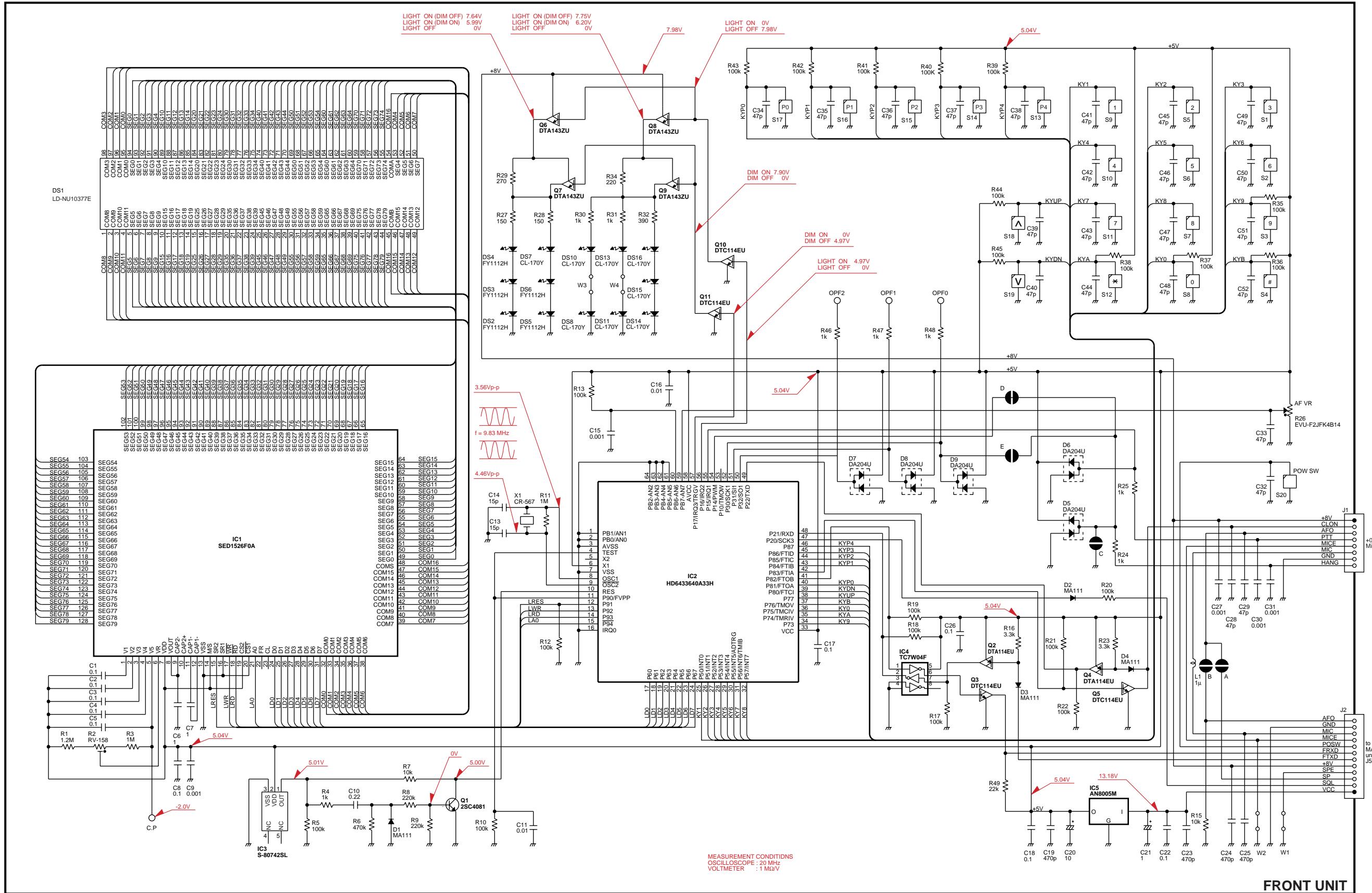


SECTION 10 BLOCK DIAGRAM

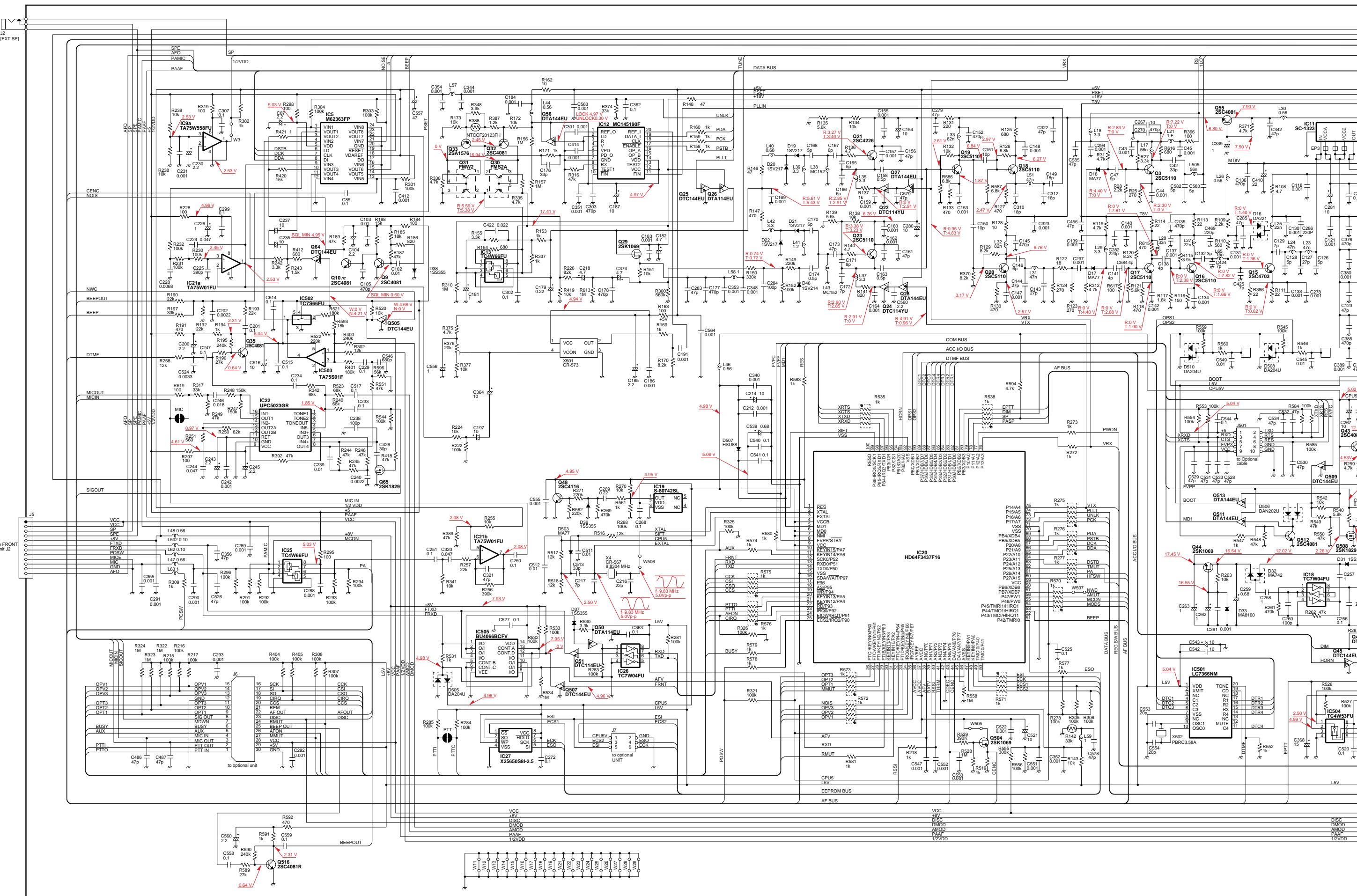


SECTION 11 VOLTAGE DIAGRAM

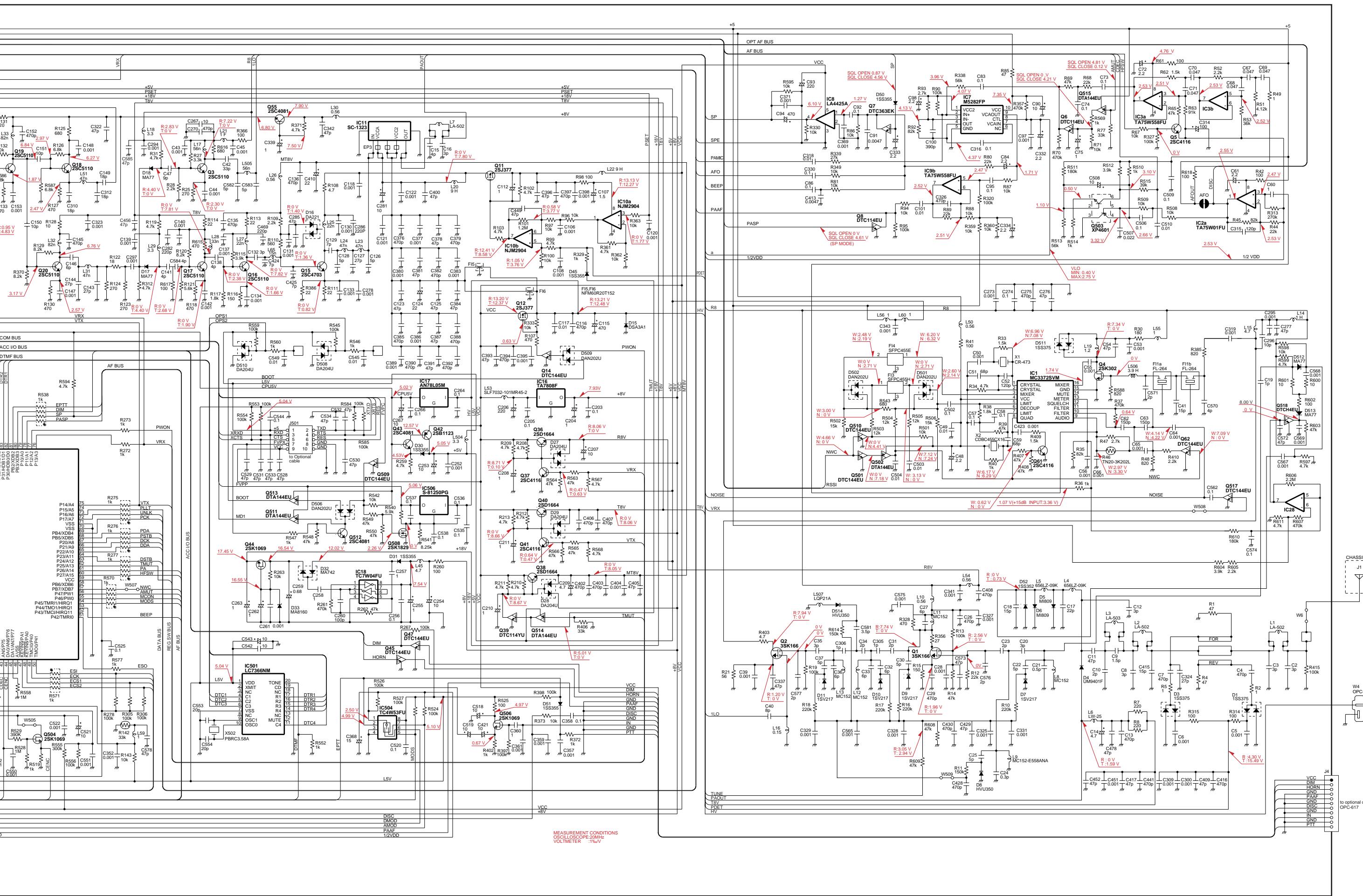
11-1 FRONT UNIT



11-2 MAIN UNIT

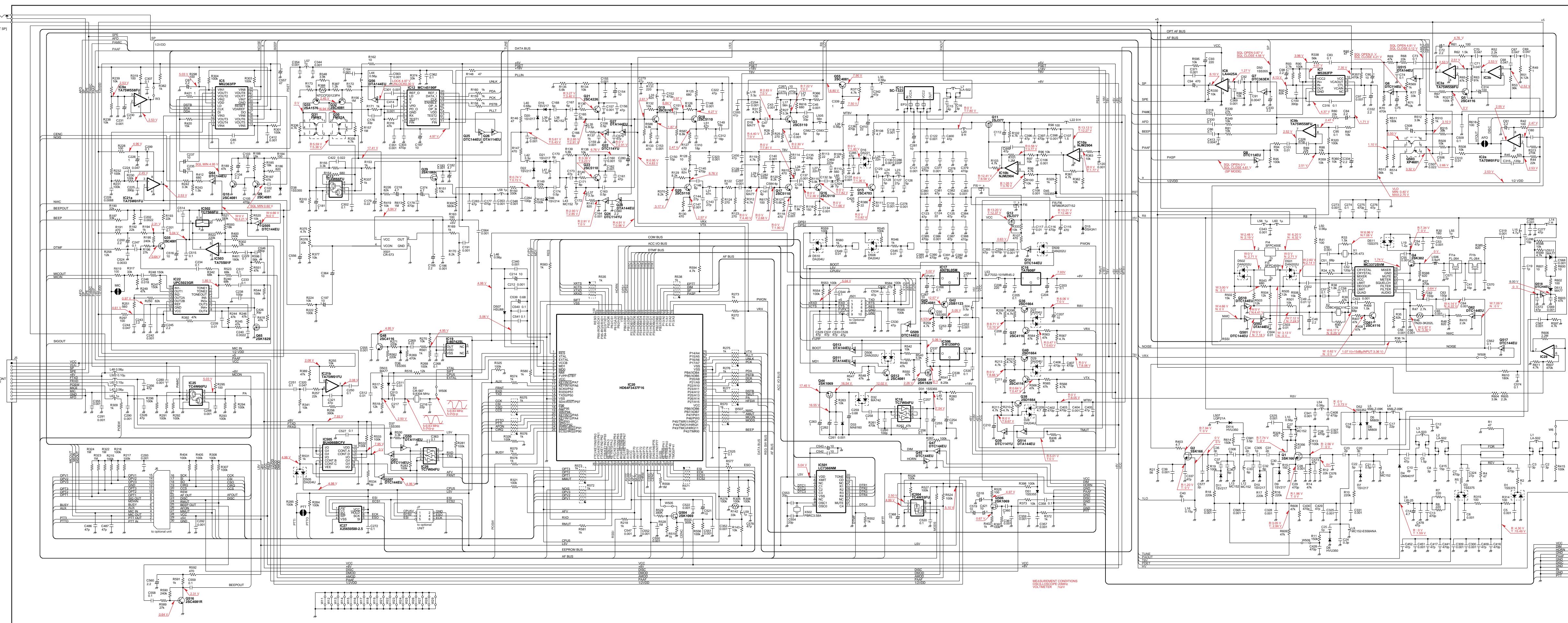


COMPLETE VIEW



COMPLETE VIEW

1-2 MAIN UNIT



LEFT SIDE

RIGHT SIDE

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